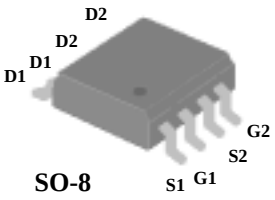
AP4575GM

Pb Free Plating Product

Advanced Power Electronics Corp.

N AND P-CHANNEL ENHANCEMENT MODE POWER MOSFET

- ▼ **Simple Drive Requirement**
- ▼ **Low On-resistance**
- ▼ **Fast Switching Performance**



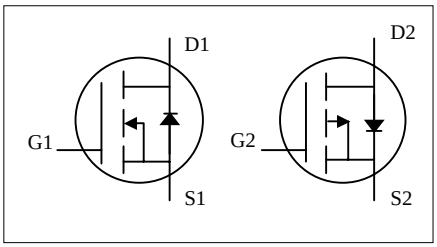
SO-8

N-CH	BV_{DSS}	60V
	$R_{DS(ON)}$	36mΩ
	I_D	6A
P-CH	BV_{DSS}	-60V
	$R_{DS(ON)}$	72mΩ
	I_D	-4.2A

Description

The Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The SO-8 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-channel	P-channel	
V_{DS}	Drain-Source Voltage	60	-60	V
V_{GS}	Gate-Source Voltage	±20	±20	V
I_D@T_A=25℃	Continuous Drain Current[3]	6	-4.2	A
I_D@T_A=70℃	Continuous Drain Current[3]	4.7	-3.3	A
I_{DM}	Pulsed Drain Current[1]	30	-30	A
P_D@T_A=25℃	Total Power Dissipation	2.0		W
	Linear Derating Factor	0.016		W/℃
T_{STG}	Storage Temperature Range	-55 to 150		℃
T_J	Operating Junction Temperature Range	-55 to 150		℃

Thermal Data

Symbol	Parameter		Value	Unit
Rthj-a	Thermal Resistance Junction-ambient[3]	Max.	62.5	℃/W

N-CH Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	60	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25℃, I_D=1mA	-	0.04	-	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V, I_D=5A	-	-	36	mΩ
		V_{GS}=4.5V, I_D=3A	-	-	42	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	1	-	3	V
g_{fs}	Forward Transconductance	V_{DS}=10V, I_D=5A	-	8	-	S
I_{DSS}	Drain-Source Leakage Current (T_j=25°C)	V_{DS}=60V, V_{GS}=0V	-	-	1	uA
	Drain-Source Leakage Current (T_j=70°C)	V_{DS}=48V, V_{GS}=0V	-	-	25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V	-	-	±100	nA
Q_g	Total Gate Charge[2]	I_D=5A	-	18	29	nC
Q_{gs}	Gate-Source Charge	V_{DS}=48V	-	5	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=4.5V	-	10	-	nC
$t_{d(on)}$	Turn-on Delay Time[2]	V_{DS}=30V	-	10	-	ns
t_r	Rise Time	I_D=1A	-	6	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω,V_{GS}=10V	-	32	-	ns
t_f	Fall Time	R_D=30Ω	-	10	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	1670	2670	pF
C_{oss}	Output Capacitance	V_{DS}=25V	-	160	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	117	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=1.7A, V_{GS}=0V	-	-	1.2	V
t_{rr}	Reverse Recovery Time[2]	I_S=5A, V_{GS}=0V	-	34	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	48	-	nC

P-CH Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=-250uA	-60	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25℃,I_D=-1mA	-	-0.04	-	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V, I_D=-4A	-	-	72	mΩ
		V_{GS}=-4.5V, I_D=-3A	-	-	88	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=-250uA	-1	-	-3	V
g_{fs}	Forward Transconductance	V_{DS}=-10V, I_D=-4A	-	6	-	S
I_{DSS}	Drain-Source Leakage Current (Tj=25°C)	V_{DS}=-60V, V_{GS}=0V	-	-	-1	uA
	Drain-Source Leakage Current (Tj=70°C)	V_{DS}=-48V, V_{GS}=0V	-	-	-25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V	-	-	±100	nA
Q_g	Total Gate Charge[2]	I_D=-4A	-	21	34	nC
Q_{gs}	Gate-Source Charge	V_{DS}=-48V	-	5	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=-4.5V	-	9	-	nC
$t_{d(on)}$	Turn-on Delay Time[2]	V_{DS}=-30V	-	12	-	ns
t_r	Rise Time	I_D=-1A	-	6	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω,V_{GS}=-10V	-	82	-	ns
t_f	Fall Time	R_D=30Ω	-	36	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	1780	2850	pF
C_{oss}	Output Capacitance	V_{DS}=-25V	-	157	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	130	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=-1.7A, V_{GS}=0V	-	-	-1.2	V
t_{rr}	Reverse Recovery Time[2]	I_S=-4A, V_{GS}=0V	-	43	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=-100A/μs	-	87	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse width ≤300us , duty cycle ≤2%.

3.Surface mounted on 1 in^2 copper pad of FR4 board ; 135℃/W when mounted on min. copper pad.

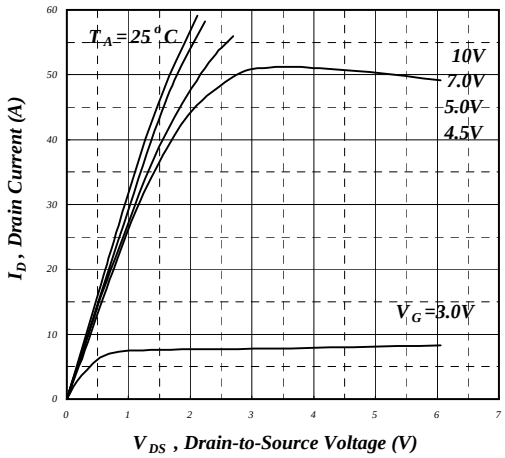


Fig 1. Typical Output Characteristics

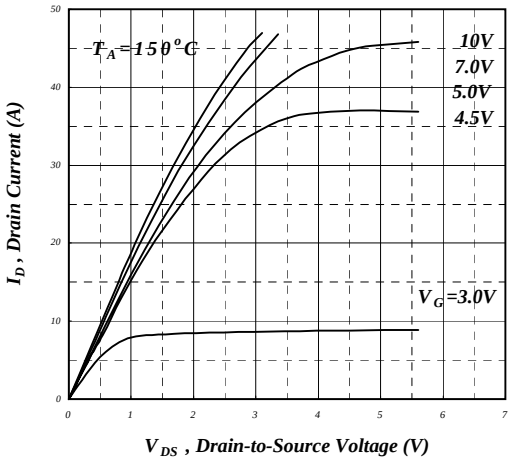


Fig 2. Typical Output Characteristics

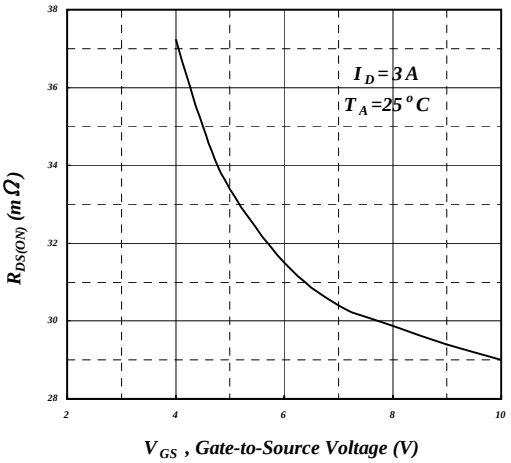


Fig 3. On-Resistance v.s. Gate Voltage

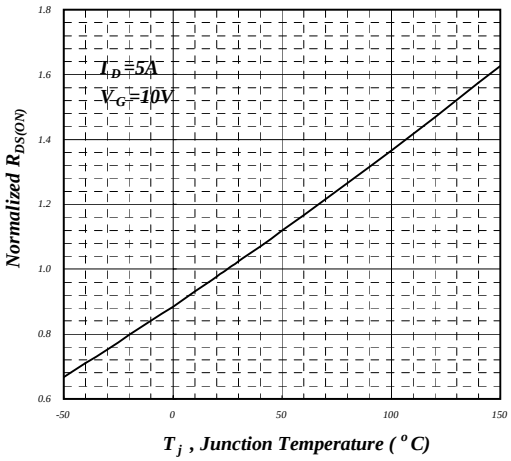


Fig 4. Normalized On-Resistance v.s. Junction Temperature

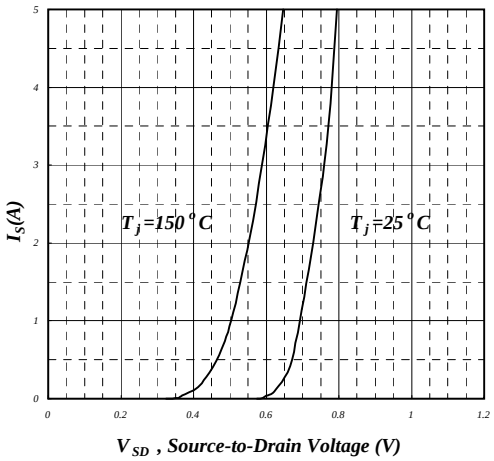


Fig 5. Forward Characteristic of Reverse Diode

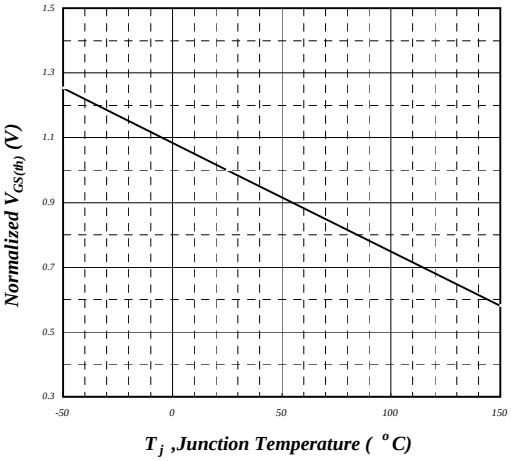


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

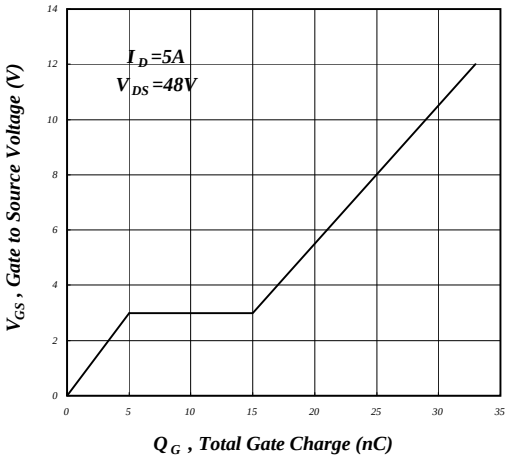


Fig 7. Gate Charge Characteristics

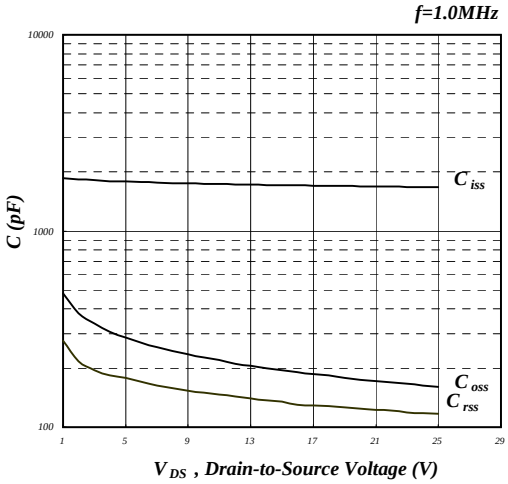


Fig 8. Typical Capacitance Characteristics

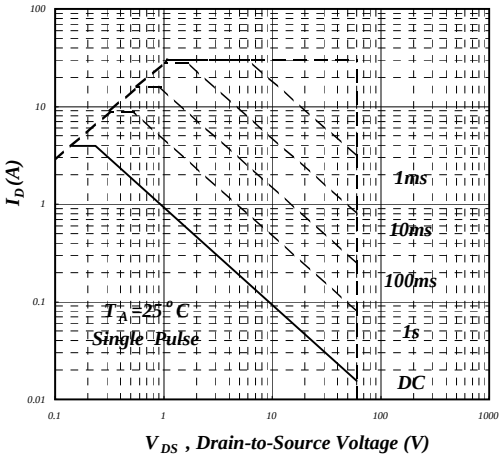


Fig 9. Maximum Safe Operating Area

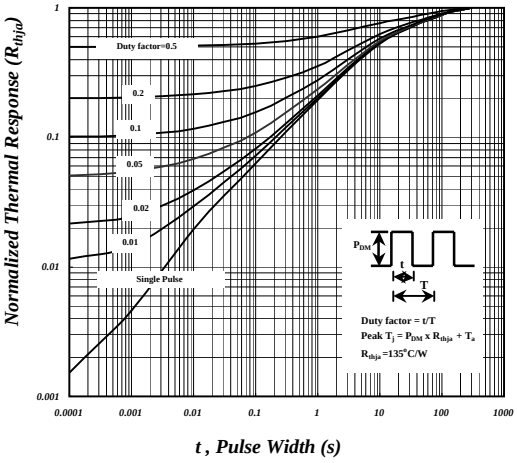


Fig 10. Effective Transient Thermal Impedance

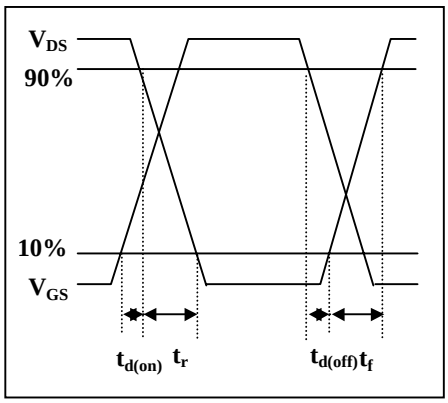


Fig 11. Switching Time Waveform

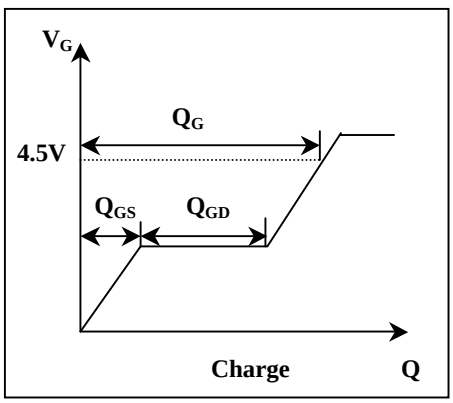


Fig 12. Gate Charge Waveform

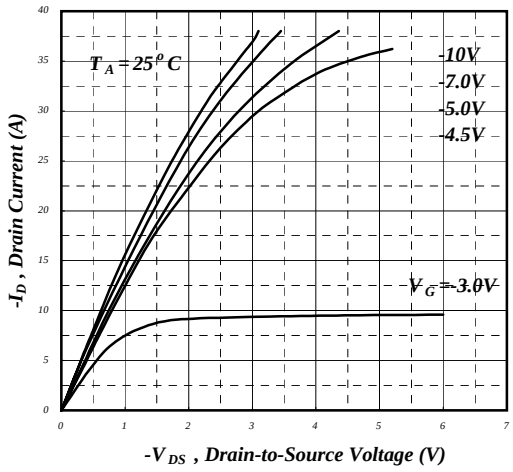


Fig 1. Typical Output Characteristics

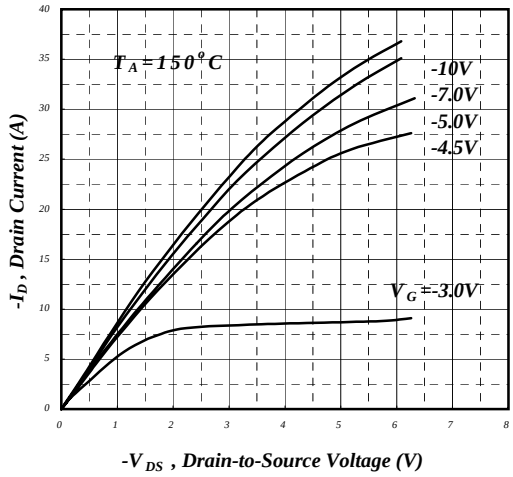


Fig 2. Typical Output Characteristics

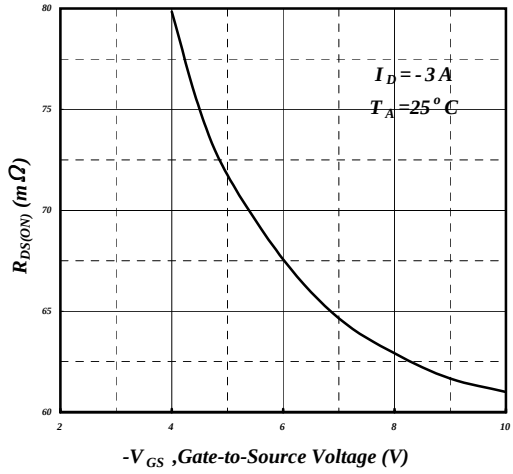


Fig 3. On-Resistance v.s. Gate Voltage

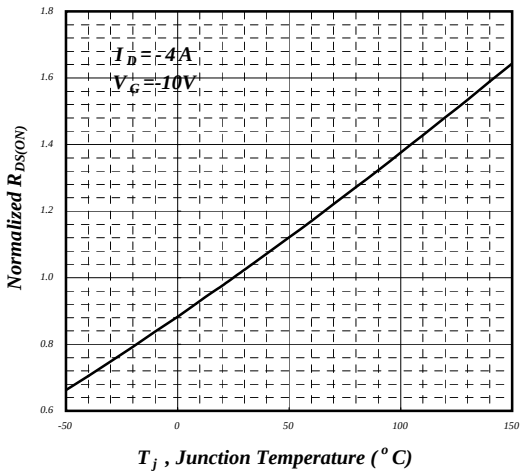


Fig 4. Normalized On-Resistance v.s. Junction Temperature

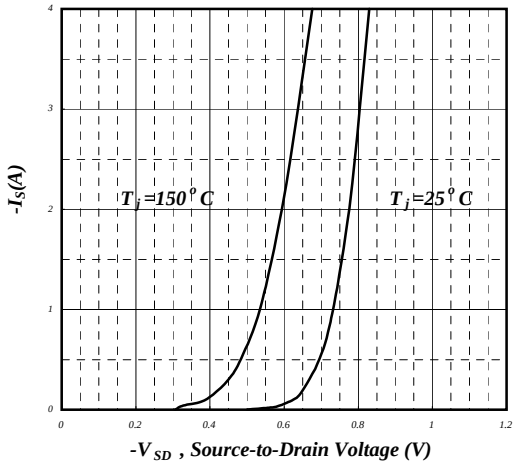


Fig 5. Forward Characteristic of Reverse Diode

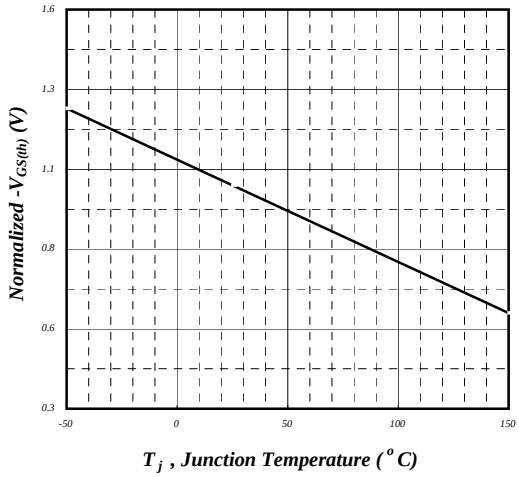


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

P-Channel

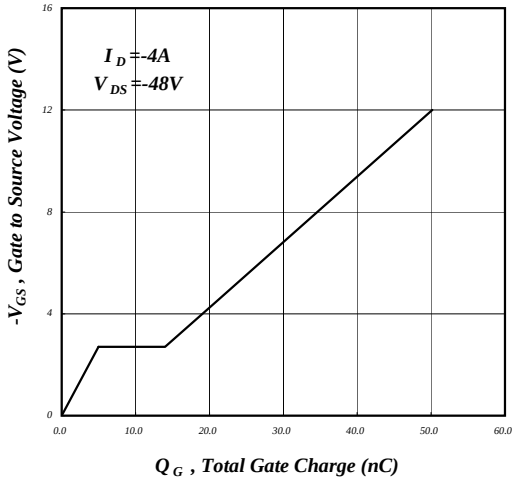


Fig 7. Gate Charge Characteristics

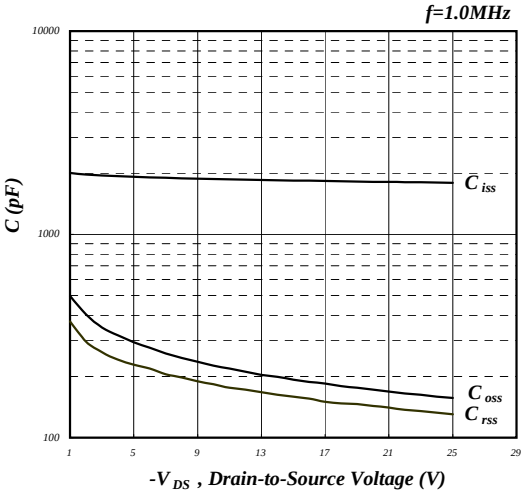


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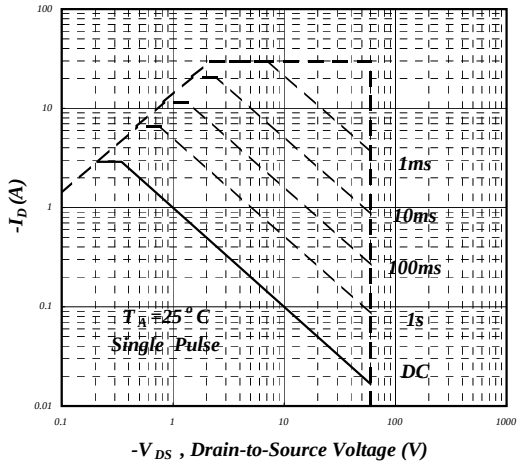


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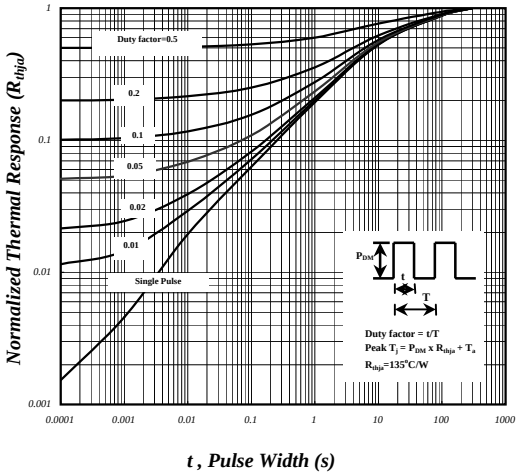


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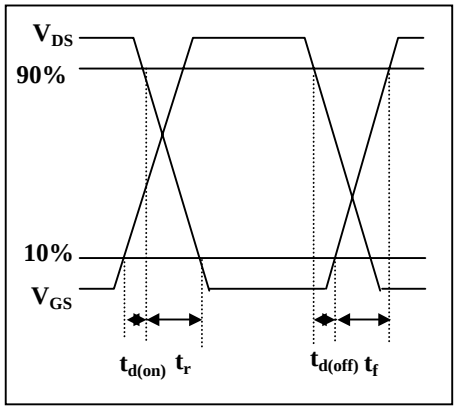


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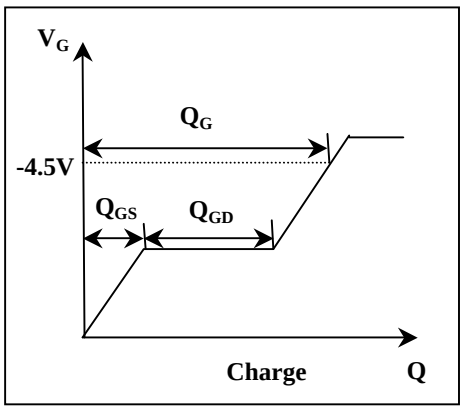


Fig 12. Gate Charge Waveform