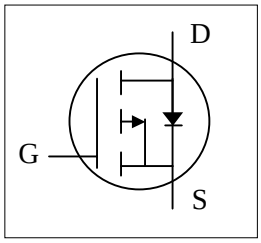
AP4439GMT-HF

Halogen-Free Product

Advanced Power Electronics Corp.

P-CHANNEL ENHANCEMENT MODE POWER MOSFET

- ▼ **Simple Drive Requirement**
- ▼ **SO-8 Compatible**
- ▼ **Lower Gate Charge**
- ▼ **RoHS Compliant & Halogen-Free**

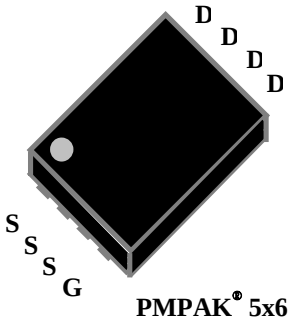


BV_{DSS}	-30V
$R_{DS(ON)}$	10mΩ
I_D	-58A

Description

AP4439 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The PMPAK® 5x6 package is special for voltage conversion application using standard infrared reflow technique with the backside heat sink to achieve the good thermal performance.



PMPAK® 5x6

Absolute Maximum Ratings@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	±25	V
I_D@T_C=25℃	Drain Current (Chip), V_{GS} @ 10V	-58	A
I_D@T_A=25℃	Drain Current[3], V_{GS} @ 10V	-18.5	A
I_D@T_A=70℃	Drain Current[3], V_{GS} @ 10V	-14.8	A
I_{DM}	Pulsed Drain Current[1]	-200	A
P_D@T_C=25℃	Total Power Dissipation	50	W
P_D@T_A=25℃	Total Power Dissipation	5	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-c	Maximum Thermal Resistance, Junction-case	2.5	℃/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient[3]	25	℃/W

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=-250uA	-30	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V, I_D=-20A	-	8	10	mΩ
		V_{GS}=-4.5V, I_D=-12A	-	11	15	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=-250uA	-1	-1.4	-3	V
g_{fs}	Forward Transconductance	V_{DS}=-10V, I_D=-20A	-	28	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-24V, V_{GS}=0V	-	-	-10	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Q_g	Total Gate Charge	I_D=-20A	-	30	48	nC
Q_{gs}	Gate-Source Charge	V_{DS}=-15V	-	6	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=-4.5V	-	15	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=-15V	-	12	-	ns
t_r	Rise Time	I_D=-1A	-	8	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω	-	100	-	ns
t_f	Fall Time	V_{GS}=-10V	-	55	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	2750	4400	pF
C_{oss}	Output Capacitance	V_{DS}=-15V	-	490	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	420	-	pF
R_g	Gate Resistance	f=1.0MHz	-	5.3	10.6	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=-20A, V_{GS}=0V	-	-	-1.2	V
t_{rr}	Reverse Recovery Time	I_S=-10A, V_{GS}=0V,	-	30	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	24	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

3.Surface mounted on 1 in^2 copper pad of FR4 board, t ≤10sec

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.
USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.
APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.
APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

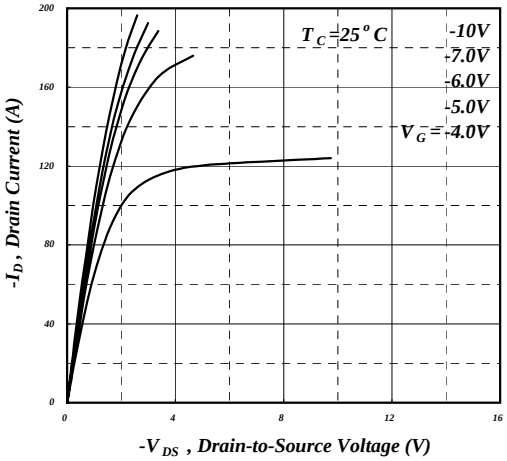


Fig 1. Typical Output Characteristics

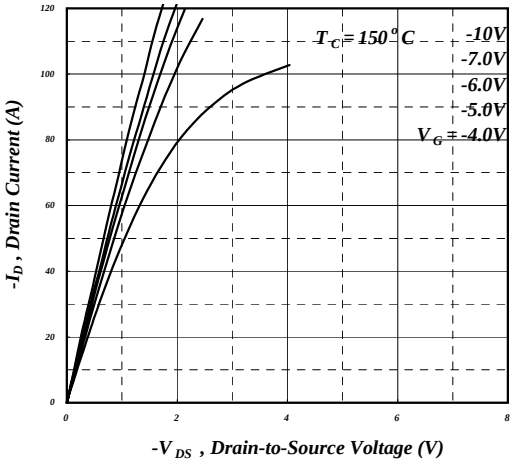


Fig 2. Typical Output Characteristics

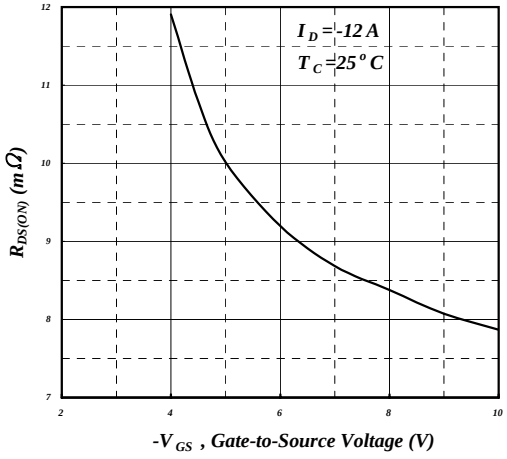


Fig 3. On-Resistance v.s. Gate Voltage

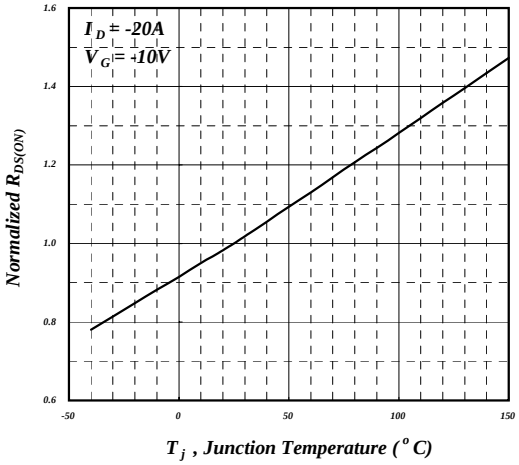


Fig 4. Normalized On-Resistance v.s. Junction Temperature

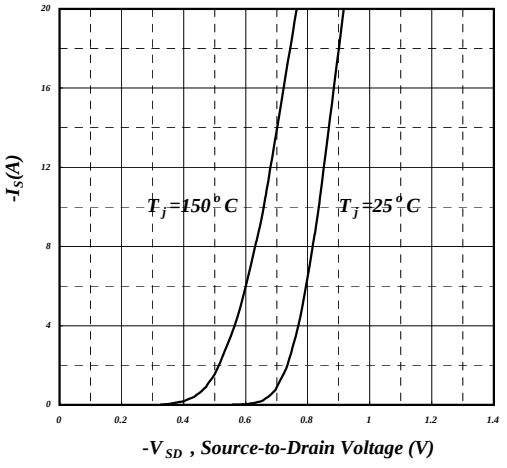


Fig 5. Forward Characteristic of Reverse Diode

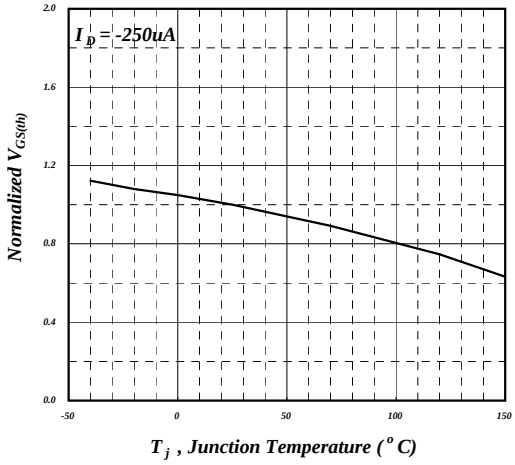


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

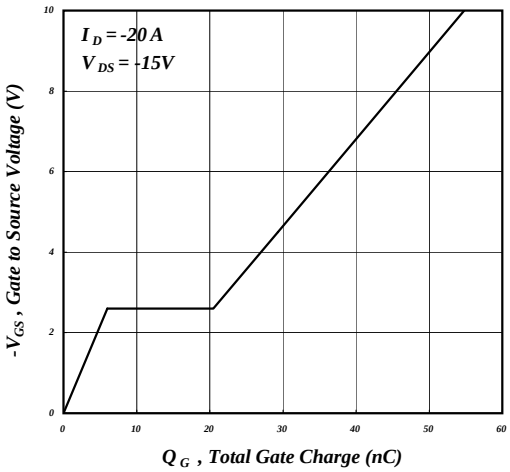


Fig 7. Gate Charge Characteristics

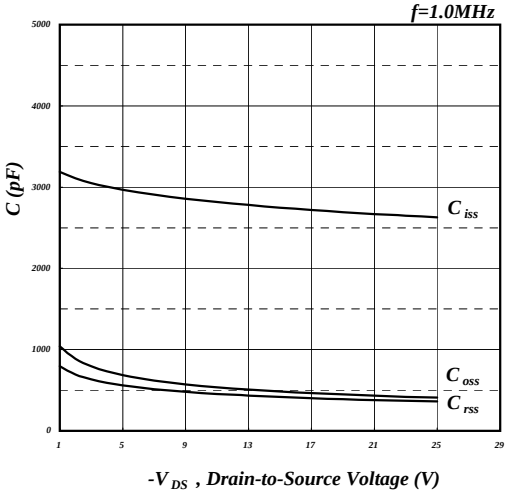


Fig 8. Typical Capacitance Characteristics

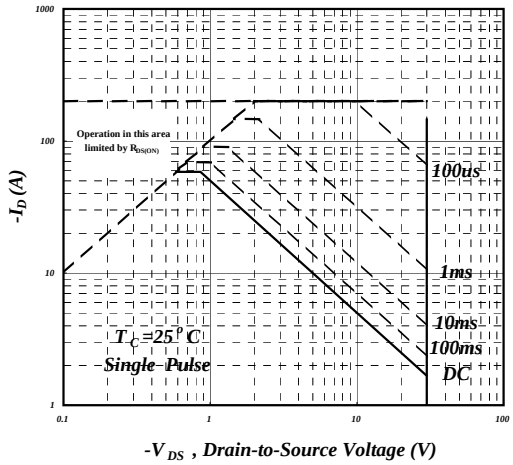


Fig 9. Maximum Safe Operating Area

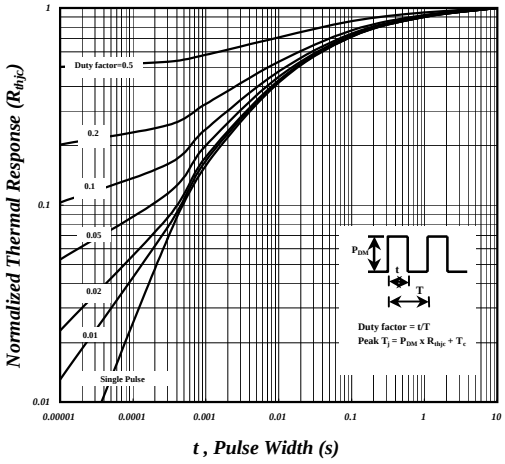


Fig 10. Effective Transient Thermal Impedance

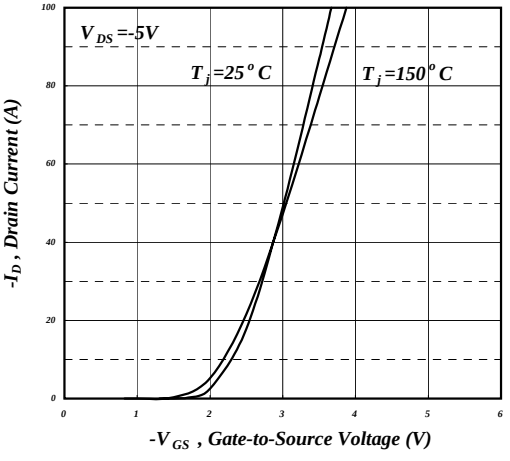


Fig 11. Transfer Characteristics

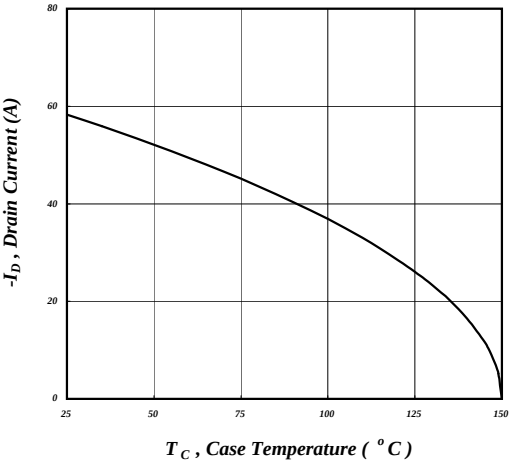
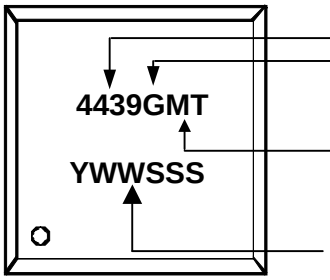


Fig 12. Drain Current v.s. Case Temperature

MARKING INFORMATION



Part Number
meet Rohs requirement
for low voltage MOSFET only
Package Code : MT
Date Code (YWWSSS)
Y：Last Digit Of The Year
WW：Week
SSS：Sequence