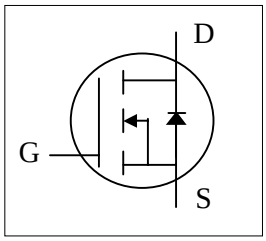
AP40T10GH-HF

Halogen-Free Product

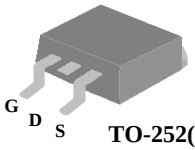
Advanced Power Electronics Corp.

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

- ▼ **100% Avalanche Test**
- ▼ **Single Drive Requirement**
- ▼ **Fast Switching Characteristic**
- ▼ **RoHS Compliant**



BV_{DSS}	100V
$R_{DS(ON)}$	35mΩ
I_D	39A



TO-252(H)

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-252 package is widely preferred for commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V	39	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V	27	A
I_{DM}	Pulsed Drain Current[1]	80	A
P_D@T_C=25℃	Total Power Dissipation	125	W
T_{STG}	Storage Temperature Range	-55 to 175	℃
T_J	Operating Junction Temperature Range	-55 to 175	℃

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	1.2	℃/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	℃/W

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=1mA	100	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V, I_D=15A	-	-	35	mΩ
		V_{GS}=6V, I_D=10A	-	-	38	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	2	-	4	V
g_{fs}	Forward Transconductance	V_{DS}=15V, I_D=15A	-	14.5	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=80V, V_{GS}=0V	-	-	10	uA
	Drain-Source Leakage Current (T_j=125°C)	V_{DS}=80V ,V_{GS}=0V	-	-	100	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V	-	-	±100	nA
Q_g	Total Gate Charge[2]	I_D=40A	-	24	40	nC
Q_{gs}	Gate-Source Charge	V_{DS}=50V	-	5.4	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=10V	-	9.6	-	nC
$t_{d(on)}$	Turn-on Delay Time[2]	V_{DS}=50V	-	9	-	ns
t_r	Rise Time	I_D=40A	-	64	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=2.5Ω,V_{GS}=10V	-	19	-	ns
t_f	Fall Time	R_D=1.25Ω	-	75	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	1310	2100	pF
C_{oss}	Output Capacitance	V_{DS}=25V	-	270	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	85	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=30A, V_{GS}=0V	-	-	1.2	V
t_{rr}	Reverse Recovery Time[2]	I_S=30A, V_{GS}=0V,	-	60	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	125	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

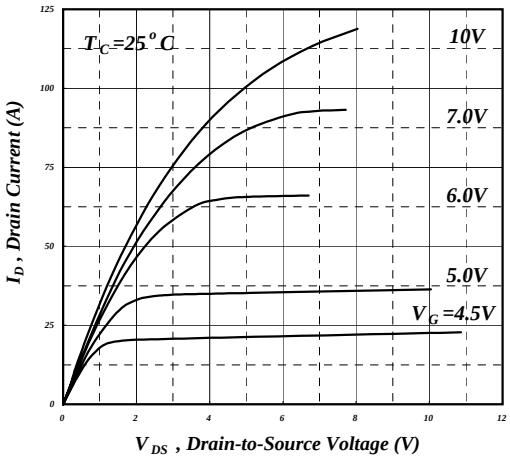


Fig 1. Typical Output Characteristics

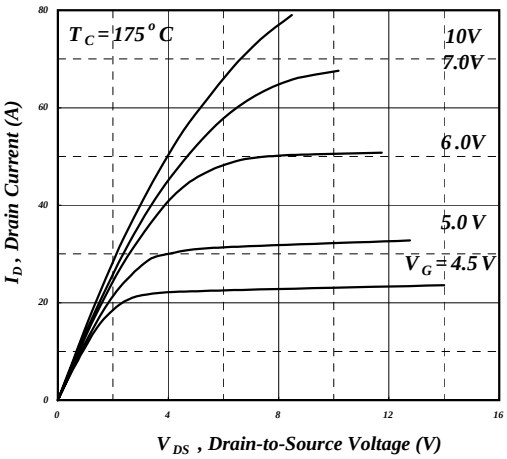


Fig 2. Typical Output Characteristics

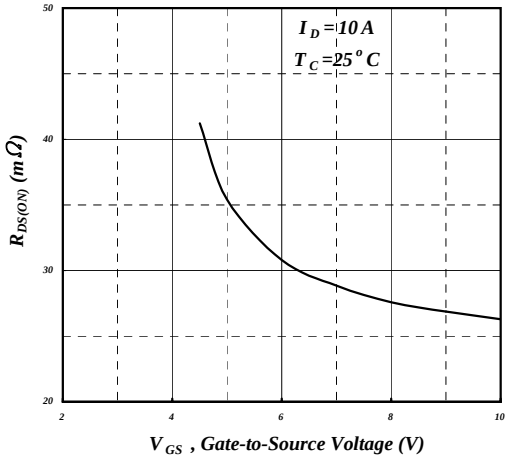


Fig 3. On-Resistance v.s. Gate Voltage

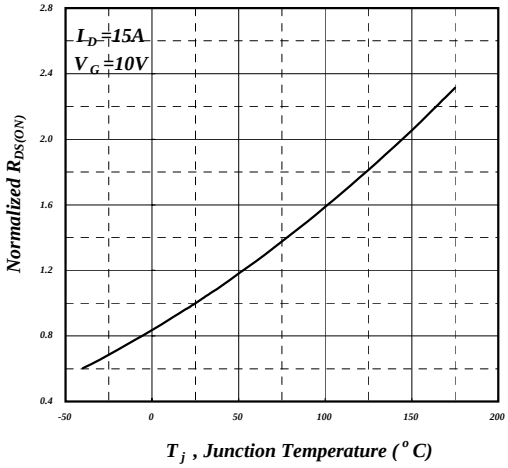


Fig 4. Normalized On-Resistance v.s. Junction Temperature

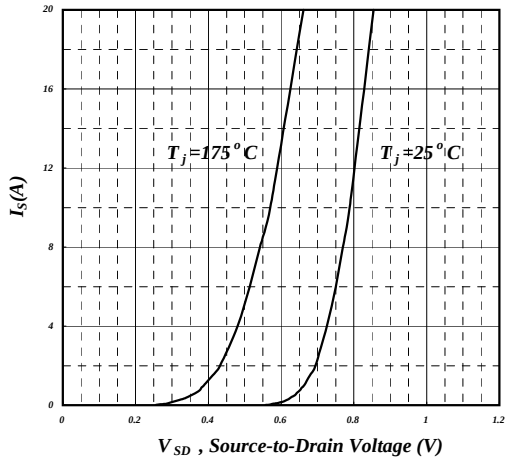


Fig 5. Forward Characteristic of Reverse Diode

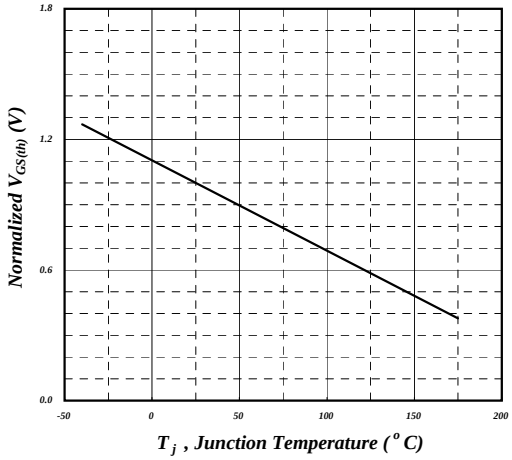


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

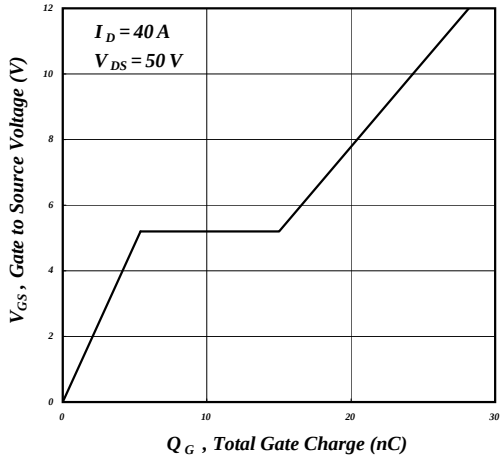


Fig 7. Gate Charge Characteristics

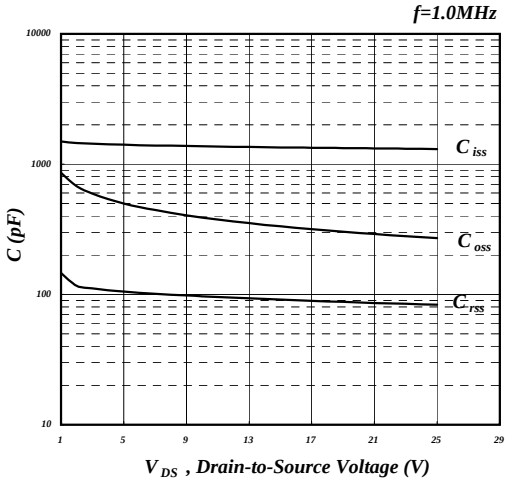


Fig 8. Typical Capacitance Characteristics

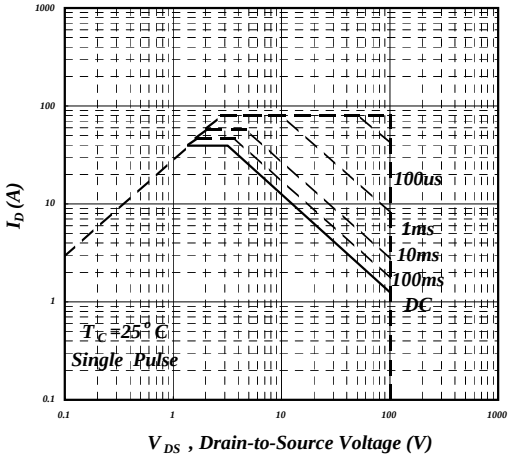


Fig 9. Maximum Safe Operating Area

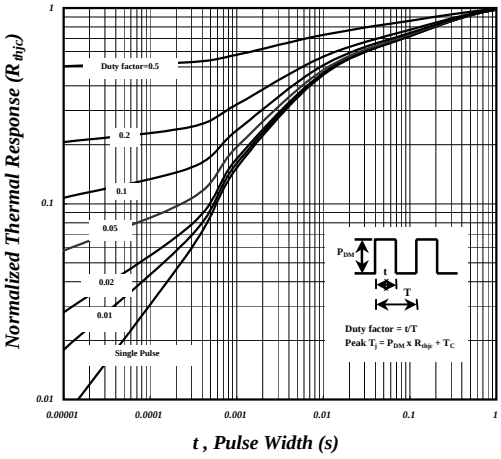


Fig 10. Effective Transient Thermal Impedance

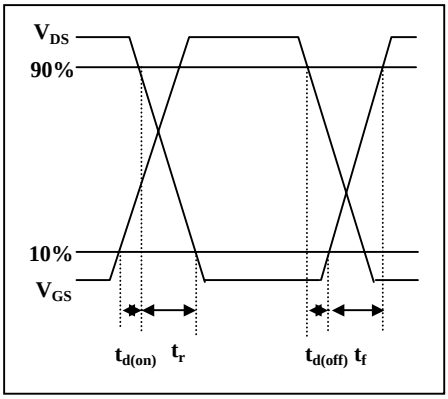


Fig 11. Switching Time Waveform

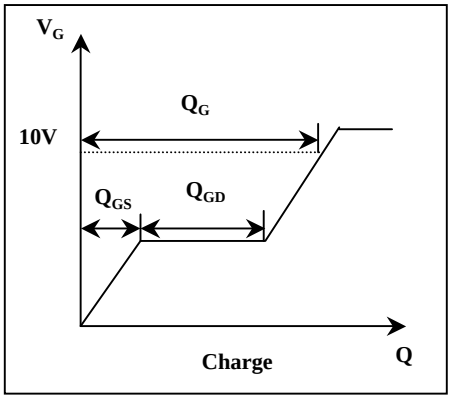


Fig 12. Gate Charge Waveform