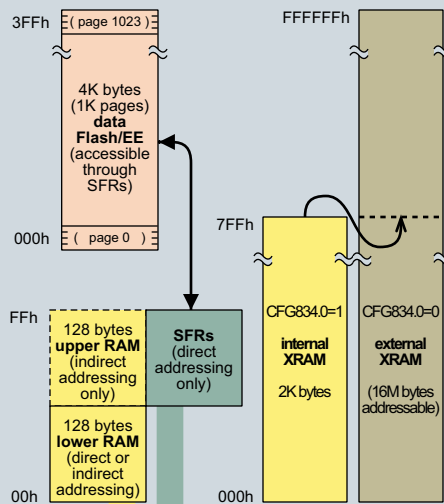


DATA MEMORY: RAM, SFRs, user Flash/EE (all read/write)

LOWER RAM

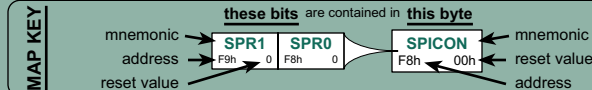
decimal address	HEX address	General Purpose Area	MSB address	LSB address
127	7Fh	Bit Addressable Area	7Fh	78h
48	30h		77h	70h
47	2Fh		76h	68h
46	2Eh		75h	60h
45	2Dh		74h	58h
44	2Ch		73h	50h
43	2Bh		72h	48h
42	2Ah		71h	40h
41	29h		70h	38h
40	28h		6Fh	30h
39	27h		6Eh	28h
38	26h		6Dh	20h
37	25h		6Ch	18h
36	24h		6Bh	10h
35	23h		6Ah	08h
34	22h		69h	00h
33	21h	Register Bank 3	68h	00h
32	20h		67h	00h
31	1Fh		66h	00h
30	1Eh		65h	00h
29	1Dh		64h	00h
28	1Ch		63h	00h
27	1Bh		62h	00h
26	1Ah		61h	00h
25	19h		60h	00h
24	18h		5Fh	00h
23	17h		5Eh	00h
22	16h		5Dh	00h
21	15h		5Ch	00h
20	14h		5Bh	00h
19	13h		5Ah	00h
18	12h		59h	00h
17	11h	Register Bank 2	58h	00h
16	10h		57h	00h
15	0Fh		56h	00h
14	0Eh		55h	00h
13	0Dh		54h	00h
12	0Ch		53h	00h
11	0Bh		52h	00h
10	0Ah		51h	00h
9	09h		50h	00h
8	08h		4Fh	00h
7	07h		4Eh	00h
6	06h		4Dh	00h
5	05h		4Ch	00h
4	04h		4Bh	00h
3	03h		4Ah	00h
2	02h		49h	00h
1	01h	Register Bank 1	48h	00h
0	00h		47h	00h
			46h	00h
			45h	00h
			44h	00h
			43h	00h
			42h	00h
			41h	00h
			40h	00h
			3Fh	00h
			3Eh	00h
			3Dh	00h
			3Ch	00h
			3Bh	00h
			3Ah	00h
			39h	00h
			38h	00h
			37h	00h
			36h	00h
			35h	00h
			34h	00h
			33h	00h
			32h	00h
			31h	00h
			30h	00h
			2Fh	00h
			2Eh	00h
			2Dh	00h
			2Ch	00h
			2Bh	00h
			2Ah	00h
			29h	00h
			28h	00h
			27h	00h
			26h	00h
			25h	00h
			24h	00h
			23h	00h
			22h	00h
			21h	00h
			20h	00h
			1Fh	00h
			1Eh	00h
			1Dh	00h
			1Ch	00h
			1Bh	00h
			1Ah	00h
			19h	00h
			18h	00h
			17h	00h
			16h	00h
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			13h	00h
			12h	00h
			11h	00h
			10h	00h
			0Fh	00h
			0Eh	00h
			0Dh	00h
			0Ch	00h
			0Bh	00h
			0Ah	00h
			09h	00h
			08h	00h
			07h	00h
			06h	00h
			05h	00h
			04h	00h
			03h	00h
			02h	00h
			01h	00h
			00h	00h

DATA MEMORY SPACE (read/write area)



SFR MAP & RESET VALUES

decimal address	HEX address	SFR Name	Reset Value
127	7Fh	SPIDAT	00h
48	30h	(reserved)	(reserved)
47	2Fh	(reserved)	(reserved)
46	2Eh	(reserved)	(reserved)
45	2Dh	(reserved)	(reserved)
44	2Ch	(reserved)	(reserved)
43	2Bh	(reserved)	(reserved)
42	2Ah	(reserved)	(reserved)
41	29h	(reserved)	(reserved)
40	28h	(reserved)	(reserved)
39	27h	(reserved)	(reserved)
38	26h	(reserved)	(reserved)
37	25h	(reserved)	(reserved)
36	24h	(reserved)	(reserved)
35	23h	(reserved)	(reserved)
34	22h	(reserved)	(reserved)
33	21h	(reserved)	(reserved)
32	20h	(reserved)	(reserved)
31	1Fh	(reserved)	(reserved)
30	1Eh	(reserved)	(reserved)
29	1Dh	(reserved)	(reserved)
28	1Ch	(reserved)	(reserved)
27	1Bh	(reserved)	(reserved)
26	1Ah	(reserved)	(reserved)
25	19h	(reserved)	(reserved)
24	18h	(reserved)	(reserved)
23	17h	(reserved)	(reserved)
22	16h	(reserved)	(reserved)
21	15h	(reserved)	(reserved)
20	14h	(reserved)	(reserved)
19	13h	(reserved)	(reserved)
18	12h	(reserved)	(reserved)
17	11h	(reserved)	(reserved)
16	10h	(reserved)	(reserved)
15	0Fh	(reserved)	(reserved)
14	0Eh	(reserved)	(reserved)
13	0Dh	(reserved)	(reserved)
12	0Ch	(reserved)	(reserved)
11	0Bh	(reserved)	(reserved)
10	0Ah	(reserved)	(reserved)
9	09h	(reserved)	(reserved)
8	08h	(reserved)	(reserved)
7	07h	(reserved)	(reserved)
6	06h	(reserved)	(reserved)
5	05h	(reserved)	(reserved)
4	04h	(reserved)	(reserved)
3	03h	(reserved)	(reserved)
2	02h	(reserved)	(reserved)
1	01h	(reserved)	(reserved)
0	00h	(reserved)	(reserved)



* calibration coefficients are preconfigured at power-up to factory calibrated values

SFR DESCRIPTIONS

ADCSTAT ADC Status Register RDY0 primary ADC ready flag RDY1 auxiliary ADC ready flag CAL calibration flag NOXREF no external reference flag ERR0 primary ADC error flag ERR1 auxiliary ADC error flag	ADCMODE ADC Mode Register ADMODE0 primary ADC enable bit ADMODE1 auxiliary ADC enable bit ADMODE2 mode bits ADMODE3 [powerdown, idle, single-conv, conv-conv, zero-select, fs-select, zero-syscal, fs-syscal]	ADCON0 Primary ADC Control Register ADCON0 external reference select bit (0=internal ref) ADCON1 channel selection bits ADCON2 [AIN1-AIN2, AIN3-AIN4, AIN5-AIN6, AIN7-AIN8] ADCON3 unipolar select bit (0=bipolar) ADCON4 [s20mV, s40mV, s80mV, s160mV, s320mV, s640mV, s128V, s256V]	ADCON1 Auxiliary ADC Control Register ADCON1 external reference select bit (0=internal ref) ADCON2 channel selection bits ADCON3 [AIN3, AIN4, TEMP_AIN5] ADCON4 unipolar select bit (0=bipolar)	SF Sync Register: f _{ADC} = 4,096Hz / (3-SF) OF0H, OF0M, OF0L ADC0 offset coefficient OF1H, OF1L ADC1 offset coefficient GNOH, GNOH, GNOH ADC0 gain coefficient GNIH, GNIH ADC1 gain coefficient ADCON0, ADCON0, ADCON0 ADC0 data ADCON1, ADCON1 ADC1 data	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 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(0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 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(0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP Interrupt Priority Register IP0 priority of P0 (0=lowest) IP1 priority of P1 (0=lowest) IP2 priority of P2 (0=lowest) IP3 priority of P3 (0=lowest) IP4 priority of P4 (0=lowest) IP5 priority of P5 (0=lowest) IP6 priority of P6 (0=lowest) IP7 priority of P7 (0=lowest)	IP
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