

FCI CONFIDENTIAL

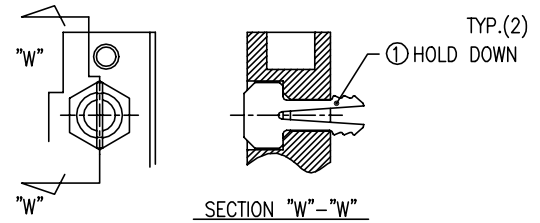


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PRODUCT NO.
92140

PRODUCT NO.	STAND OFF	SOLDER TAIL	DIM "p" (mm)	DIM "q" (mm)	FOR LOWER DECK OR UPPER DECK	① HOLD DOWN	② LOCATOR FOR SOLDER TAIL ALIGNMENT	SHEET NO.	MOUNTING STYLE TO PCB
92140-XXXX	DIM "M" (mm)								
000	0	R/A	2.8	--	LOWER	NONE	NONE	ND2	TOP MOUNT 92140-XXXX
000M	0	R/A	2.8	--	LOWER	NONE	NONE		
000LF	0	R/A	2.8	--	LOWER	NONE	NONE		
000MLF	0	R/A	2.8	--	LOWER	NONE	NONE		
000H	0	R/A	2.8	--	LOWER	YES	NONE		
000HLF	0	R/A	2.8	--	LOWER	YES	NONE		
001	0	SMT STG	--	--	LOWER	NONE	NONE		
001LF	0	SMT STG	--	--	LOWER	NONE	NONE		
001H	0	SMT STG	--	--	LOWER	YES	NONE		
001HLF	0	SMT STG	--	--	LOWER	YES	NONE		
002	0	SMT IL	--	--	LOWER	NONE	NONE		
002LF	0	SMT IL	--	--	LOWER	NONE	NONE		
002H	0	SMT IL	--	--	LOWER	YES	NONE	ND3	BOTTOM MOUNT 92140-XXXX
002HLF	0	SMT IL	--	--	LOWER	YES	NONE		
010	4	R/A	2.8	--	LOWER	NONE	YES		
010LF	4	R/A	2.8	--	LOWER	NONE	YES		
010H	4	R/A	2.8	--	LOWER	YES	YES		
010HLF	4	R/A	2.8	--	LOWER	YES	YES		
020	5	R/A	2.8	--	LOWER	NONE	YES		
020LF	5	R/A	2.8	--	LOWER	NONE	YES		
020MLF	5	R/A	2.8	--	LOWER	NONE	YES		
021MLF	5	R/A	2.8	--	LOWER	NONE	YES		
020H	5	R/A	2.8	--	LOWER	YES	YES		
020HLF	5	R/A	2.8	--	LOWER	YES	YES		
040	2	R/A	2.5	--	LOWER	NONE	NONE	ND4	BOTTOM MOUNT 92140-XXXX
040LF	2	R/A	2.5	--	LOWER	NONE	NONE		
040HLF	2	R/A	2.5	--	LOWER	YES	NONE		
050	3	R/A	2.8	--	LOWER	NONE	YES		
050LF	3	R/A	2.8	--	LOWER	NONE	YES		
050H	3	R/A	2.8	--	LOWER	YES	YES		
050HLF	3	R/A	2.8	--	LOWER	YES	YES		
005	FDR 0	R/A	2.8	8.6	UPPER	NONE	YES		
005LF	FDR 0	R/A	2.8	8.6	UPPER	NONE	YES		
045	FDR 2	R/A	2.5	10.3	UPPER	NONE	YES		
045LF	FDR 2	R/A	2.5	10.3	UPPER	NONE	YES		
500	0	R/A	2.8	--	LOWER	NONE	NONE	ND5	BOTTOM MOUNT 92140-XXXX
500LF	0	R/A	2.8	--	LOWER	NONE	NONE		
500H	0	R/A	2.8	--	LOWER	YES	NONE		
500HLF	0	R/A	2.8	--	LOWER	YES	NONE		
501	0	SMT STG	--	--	LOWER	NONE	NONE		
501LF	0	SMT STG	--	--	LOWER	NONE	NONE		
501H	0	SMT STG	--	--	LOWER	YES	NONE		
501HLF	0	SMT STG	--	--	LOWER	YES	NONE		
502	0	SMT IL	--	--	LOWER	NONE	NONE		
502LF	0	SMT IL	--	--	LOWER	NONE	NONE		
502H	0	SMT IL	--	--	LOWER	YES	NONE		
502HLF	0	SMT IL	--	--	LOWER	YES	NONE		
503	0	SMT IL	--	--	LOWER	NONE	NONE	ND6	BOTTOM MOUNT 92140-XXXX
503LF	0	SMT IL	--	--	LOWER	NONE	NONE		
510	4	R/A	2.8	--	LOWER	NONE	YES		
510LF	4	R/A	2.8	--	LOWER	NONE	YES		
510HLF	4	R/A	2.8	--	LOWER	YES	YES		
520	5	R/A	2.8	--	LOWER	NONE	YES		
520LF	5	R/A	2.8	--	LOWER	NONE	YES		
520H	5	R/A	2.8	--	LOWER	YES	YES		
520HLF	5	R/A	2.8	--	LOWER	YES	YES		
530	2	R/A	1.48	--	LOWER	NONE	NONE		
530LF	2	R/A	1.48	--	LOWER	NONE	NONE		
540	2	R/A	2.5	--	LOWER	NONE	NONE	ND7	BOTTOM MOUNT 92140-XXXX
540LF	2	R/A	2.5	--	LOWER	NONE	NONE		
505	FDR 0	R/A	2.8	8.6	UPPER	NONE	YES		
505LF	FDR 0	R/A	2.8	8.6	UPPER	NONE	YES		
535	FDR 2	R/A	1.48	9.28	UPPER	NONE	YES	ND7	BOTTOM MOUNT 92140-XXXX
535LF	FDR 2	R/A	1.48	9.28	UPPER	NONE	YES		
545	FDR 2	R/A	2.5	10.3	UPPER	NONE	YES		
545LF	FDR 2	R/A	2.5	10.3	UPPER	NONE	YES		

92140-X X X X X X
OPTIONAL LEED FREE NONE : N 0
LF : YES
OPTIONAL HOLD DOWN NONE : N 0
H : YES (SEE FIGURE OF -xxxH)
LOW COST VERSION NONE : N 0
M : YES (DETAIL AS COMPONENT)
SOLDER TAIL 0 : R/A (FOR LOWER & SINGLE)
1 : SMT STAGGERED
2 : SMT IN LINE
3 : SMT IN LINE (FOR UPPER ONLY)
5 : R/A
STAND OFF 0 : 0mm
1 : 4mm
2 : 5mm
3 : 2mm (SPECIAL TAIL LENGTH)
4 : 2mm
5 : 3mm
MOUNTING STYLE 0 : TOP MOUNTING
5 : BOTTOM MOUNTING

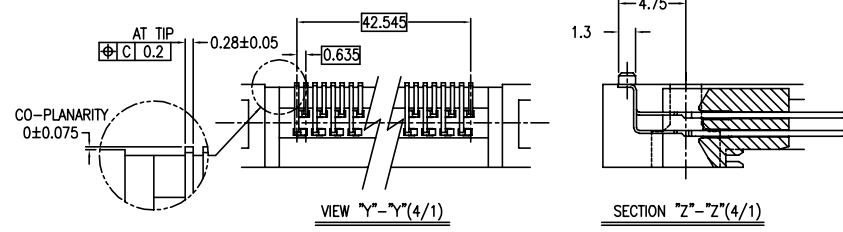
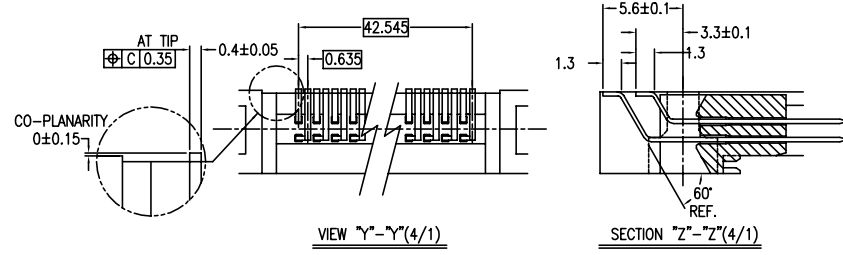
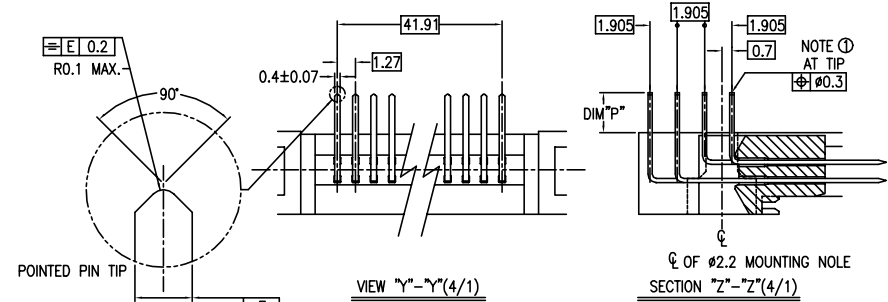
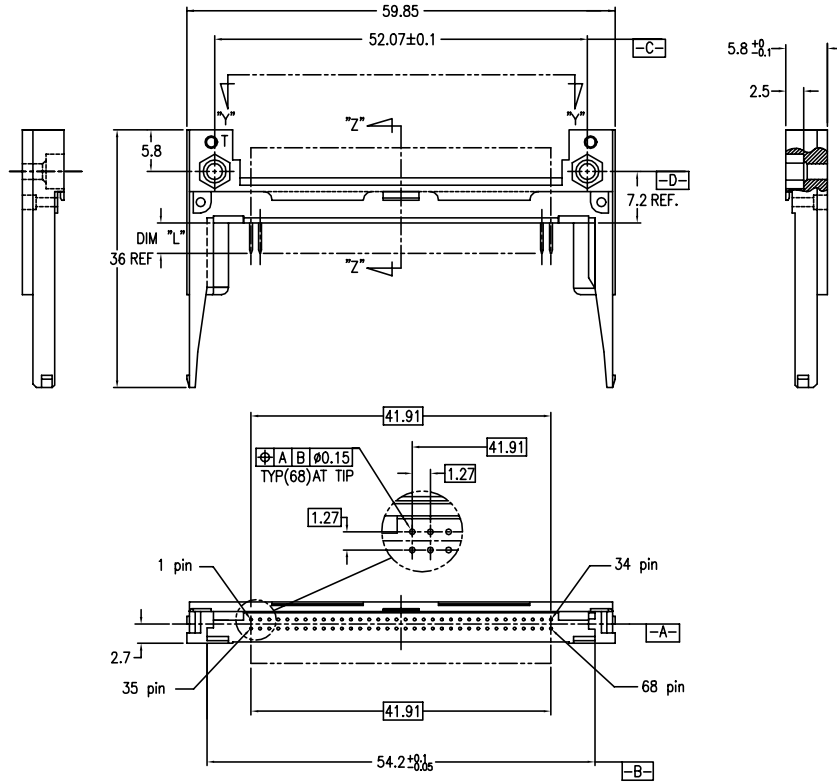


92140-xxxH
(OPTIONAL HOLD DOWN)

NOTE
1. THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008.

mat'l. code		surface		tolerance		projection		product family	
ISO 1302		ISO 406		ISO 1101		ISO 1101		PCMCIA	
l	tr	ec	n	o	d	r	date	tolerances unless otherwise specified	
AD	N06-0286	MZ	11/22/06	angles	linear	±0.3	MM	68 PDS EJECTOR HEADER ASSY FOR 5.0V APPLICATION	
AE	N07-0026	D T	1/29/07	±2'	linear	±0.3	MM	68 PDS EJECTOR HEADER ASSY FOR 5.0V APPLICATION	
AF	N07-0114	C M	05/14/07	±2'	linear	±0.3	MM	68 PDS EJECTOR HEADER ASSY FOR 5.0V APPLICATION	
AG	N07-0209	MZ	11/14/08	dr	Mike Zhou	11/21/06	11/21/06	dwg no sheet1 of 7 size	
AH	N08-0219	MZ	11/14/08	enr	Mike Zhou	11/21/06	11/21/06	92140 A4	
AJ	N09-0045	SH	05/26/09	chr	David Qin	11/21/06	11/21/06	type CUSTOMER Drawing	
sheet		revision	AJ	AJ	AJ	AJ	AJ		
Index		sheet	1	2	3	4	5	6	7

PRODUCT NO.
92140-000M
92140-000MLF
92140-000H
92140-000HLF
92140-001
92140-001LF
92140-001H
92140-001HLF
92140-002
92140-002LF
92140-002H
92140-002HLF



- NOTES
- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 - SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL
HOUSING : LCP UL94V-0 BLACK(SPECIAL VERSION MATERIAL: PA46).
PIN : PHOSPHOR BRONZE(SPECIAL VERSION MATERIAL:BRASS).
HOLD-DOWN : BRASS
 - FINISH
PIN
UNDER PLATING : 0.5μm MIN. Ni.
CONTACT AREA : 0.1μm MIN. Au.
OVER 0.5μm MIN. Pb-Ni.
NOTE:SPECIAL VERSION:ONLY 0.25μm Au
SOLDER AREA : 2.5μm MIN. Sn-Pb.
OR 2.5μm MIN. PURE Sn.(FOR -XXLF)
HOLD-DOWN : 2.5μm MIN. PURE Sn.

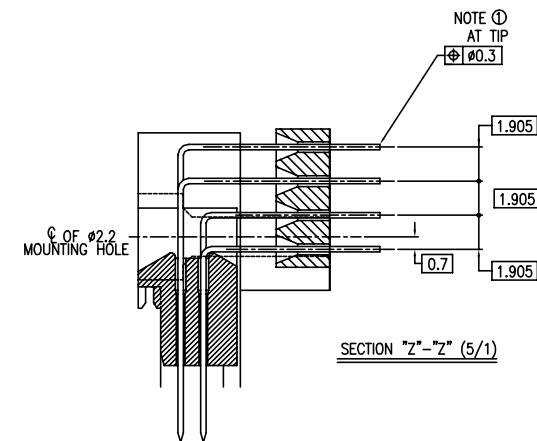
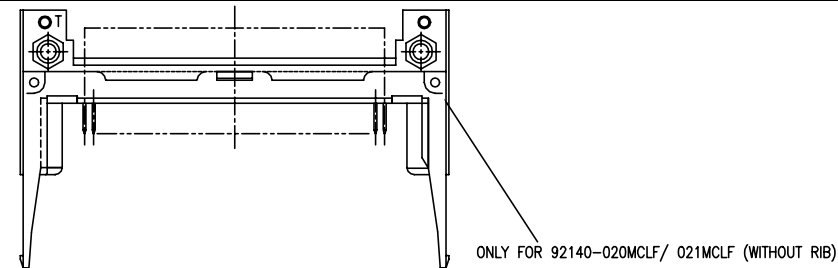
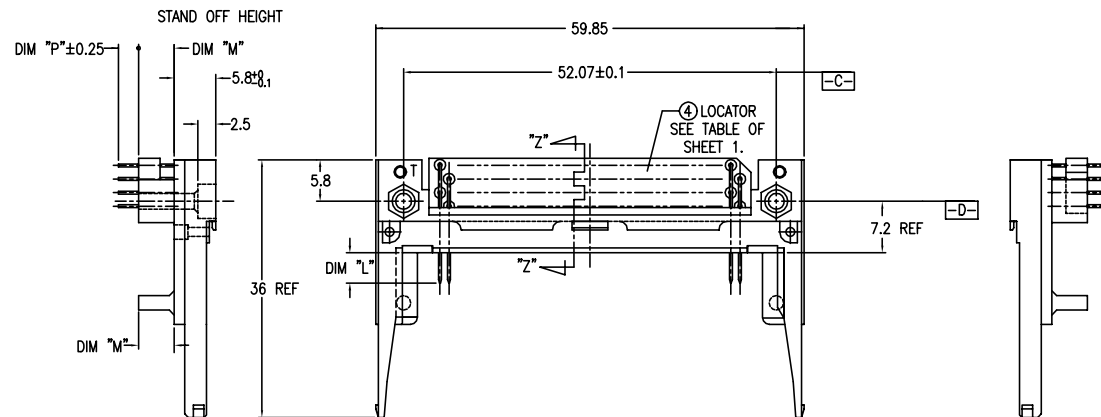
5. RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
6. SEQUENCE PIN ASSIGNMENT

	DIM "M"
	4.25±0.1
	3.5±0.1
	5.0±0.1
PIN No.	OTHERS
	36.67
	1.17±0.04
	35.51±0.08

7. GENERAL TOLERANCE : ±0.3
8. SEE SHEET 1 REGARDING COMPONENTS.

mat'l. code	surface	tolerance	projection	product family	PCMCIA
ISO 1302	ISO 406	ISO 1101			
date	tolerances unless otherwise specified	title			
dr	angles	linear	MM		68 PDS EJECTOR HEADER ASSY
enr	±2'	±0.3	scale		
chr	dr	Mike Zhou	11/21/06		
app	enr	Mike Zhou	11/21/06		
	chr	David Qin	11/21/06		
	app	Jack Wang	11/21/06		
sheet	revision			dwg no	92140
index	sheet			sheet	2 of 7
				size	A4
				type	CUSTOMER
				Drawing	

PRODUCT NO.	PRODUCT NO.
92140-010	92140-010LF 
92140-010H 	92140-010HLF 
92140-020	92140-020LF 
92140-020H 	92140-020HLF 
92140-040	92140-040LF 
92140-050 	92140-050LF 
92140-050H 	92140-050HLF 
92140-020MLF	92140-040HLF
92140-021MLF	

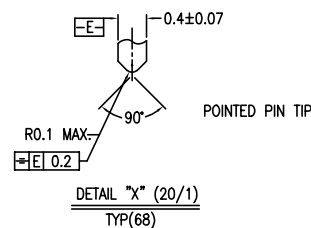
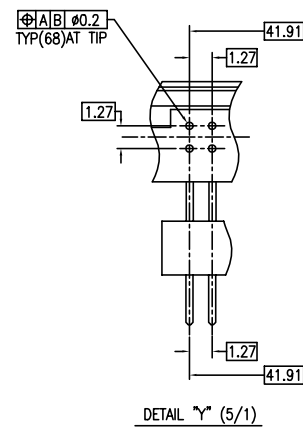


NOTES

- ① TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
2. SEE TA-946 FOR PCB LAYOUT.
- ③ MATERIAL
 - HOUSING : LCP UL94V-0 BLACK.
 - PIN : COPPER ALLOY
 - HOLD-DOWN : BRASS
- ④ FINISH PIN
 - UNDER PLATING : 0.5 μ m MIN. Ni.
 - CONTACT AREA 1 : 0.1 μ m MIN. Au.
 - OVER 0.5 μ m MIN. Pb-Ni.
 - CONTACT AREA 2 : 0.25 μ m MIN. Au(FOR -XXXMLF).
 - SOLDER AREA : 2.5 μ m MIN. Sn-Pb.
 - OR 2.5 μ m MIN. PURE Sn.(FOR -XXXLF&XXXMLF)
 - HOLD-DOWN : 2.5 μ m MIN. PURE Sn.
5. RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3 $\pm$ 0.1
6. SEQUENCE PIN ASSIGNMENT

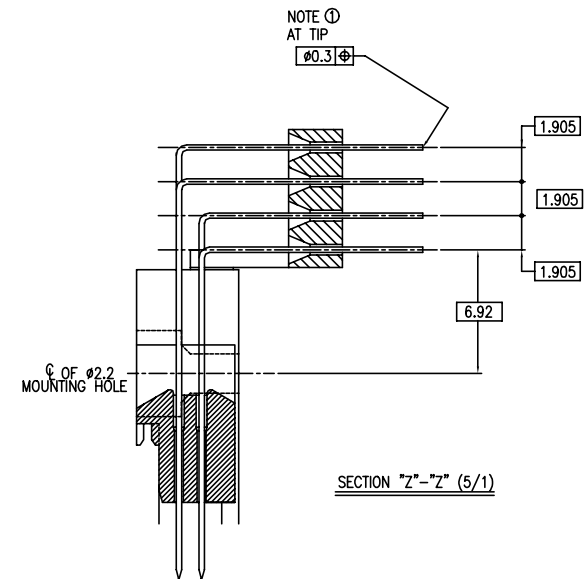
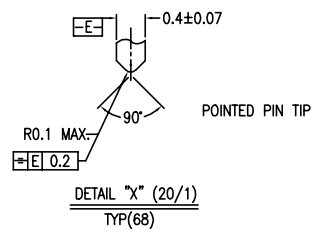
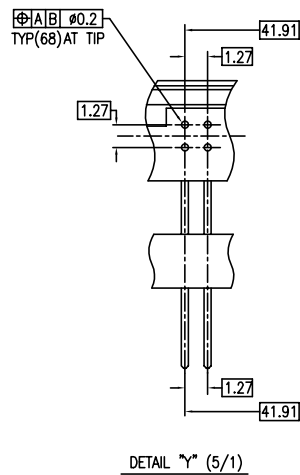
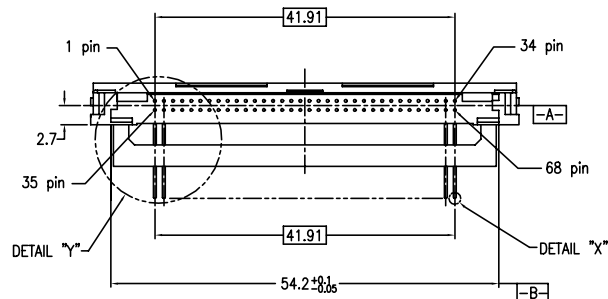
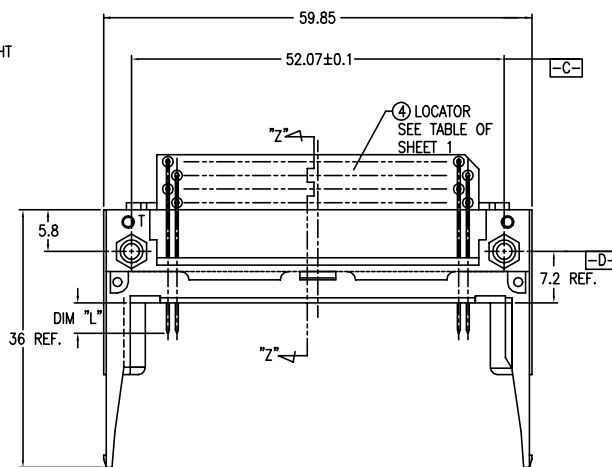
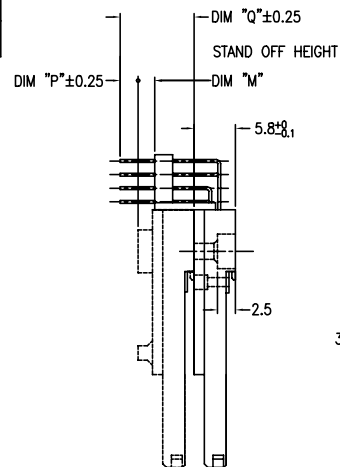
	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
PIN No.	OTHERS	36.67	1.17, 34 35.51, 68

7. GENERAL TOLERANCE : ± 0.3
8. SEE SHEET 1 REGARDING COMPONENTS.
9. THIS PRODUCT MEETS EUROPEAN UNION
DIRECTIVES AND OTHER COUNTRY
REGULATIONS AS DESCRIBED IN GS-22-008



mat'l. code				surface ISO 1302		tolerance ISO 406 ISO 1181		projection 		product family PCMCIA													
litr		ecn no		dr		date		tolerances unless otherwise specified				title											
AJ								angles linear		± 0.3		 MM		68 POS EJECTOR HEADER ASSY									
								$\pm 2'$				scale											
						dr		Mike Zhou		11/21/06				dwg no		sheet3 of 7		size					
						engr		Mike Zhou		11/21/06				92140				A4					
						chr		David Qin		11/21/06													
						appd		Jack Wang		11/21/06													
sheet index		revision sheet												type		CUSTOMER		Drawing					

PRODUCT NO.
92140-005
92140-005LF 
92140-045
92140-045LF 



NOTES

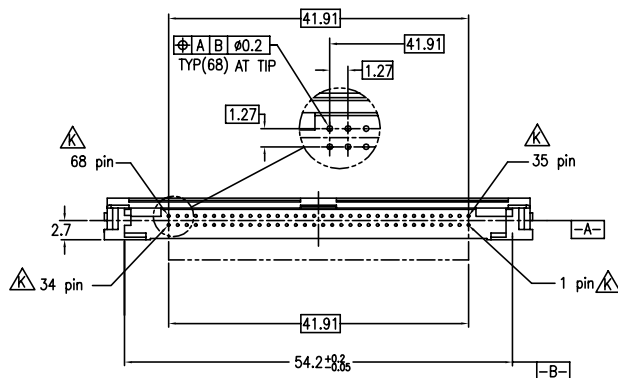
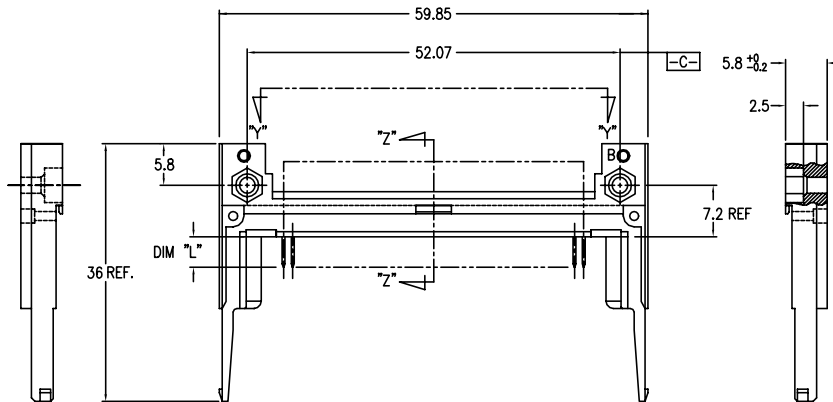
- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL
HOUSING : LCP UL94V-0 BLACK.
PIN : PHOSPHOR BRONZE.
HOLD-DOWN : BRASS
- FINISH
PIN
UNDER PLATING : 0.5μm MIN. Ni.
CONTACT AREA : 0.1μm MIN. Au.
OVER 0.5μm MIN. Pb-Ni.
SOLDER AREA : 2.5μm MIN. Sn-Pb.
OR 2.5μm MIN. PURE Sn.(FOR -XXXLF)
HOLD-DOWN : 2.5μm MIN. PURE Sn.
- RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
- SEQUENCE PIN ASSIGNMENT

PIN No.	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
OTHERS	36.67	1.17/34	35.51/68

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008

mat'l. code	surface	tolerance	projection	product family
	ISO 1302	ISO 406	ISO 1101	PCMCIA
l'tecn nldr date	tolerances unless otherwise specified			title
AJ	angles	linear	±0.3	68 PDS EJECTOR HEADER ASSY FOR 5.0V APPLICATION
	±2'		MM	
	dr Mike Zhou	11/21/06	scale	dwg no 92140 sheet 4 of 7 size A4
	enr Mike Zhou	11/21/06		
	chr David Qin	11/21/06		
	app Jack Wang	11/21/06		
sheet index	revision sheet			type CUSTOMER Drawing

PRODUCT NO.
92140-500
92140-500LF
92140-500H
92140-500HLF
92140-501
92140-501LF
92140-501H
92140-501HLF
92140-502
92140-502LF
92140-502H
92140-502HLF
92140-503
92140-503LF

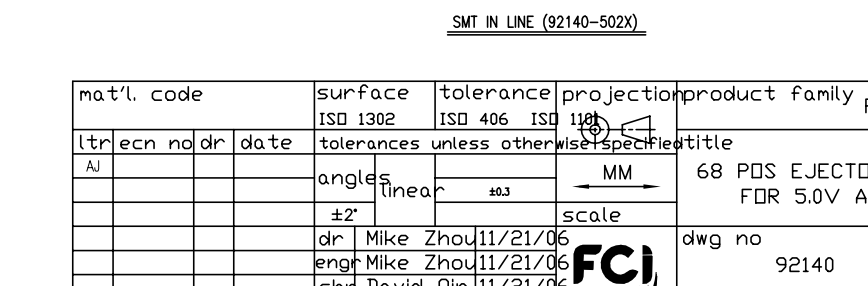
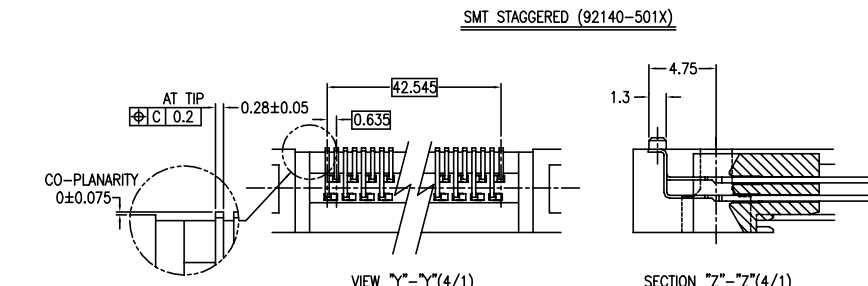
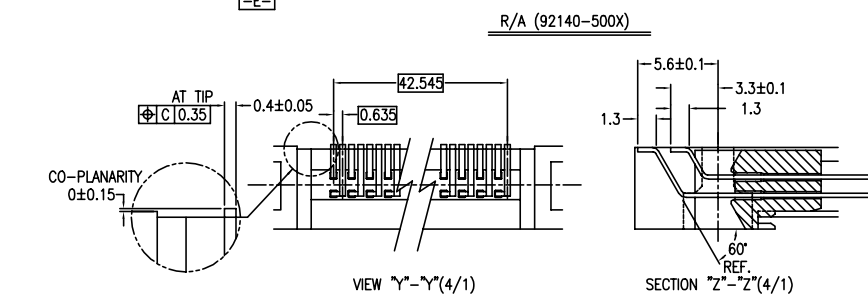
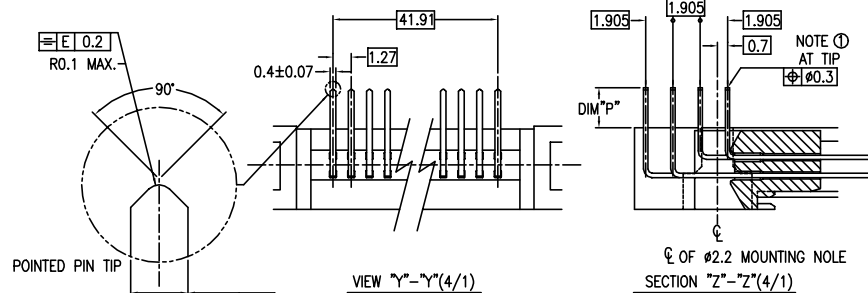


NOTES

- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL
 - HOUSING : LCP UL94V-0 NATURAL.
 - PIN : PHOSPHOR BRONZE.
 - HOLD-DOWN : BRASS
- FINISH
 - UNDER PLATING : 0.5μm MIN. Ni.
 - CONTACT AREA : 0.1μm MIN. Au.
 - OVER 0.5μm MIN. Pb-Ni.
 - SOLDER AREA : 2.5μm MIN. Sn-Pb.
 - OR 2.5μm MIN. PURE Sn.(FOR -XXXLF)
 - HOLD-DOWN : 2.5μm MIN. PURE Sn.
- RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
- SEQUENCE PIN ASSIGNMENT

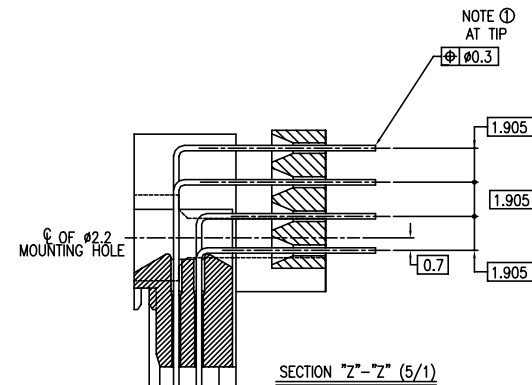
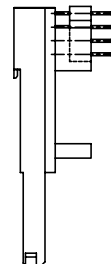
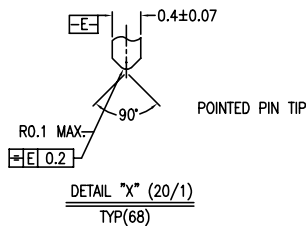
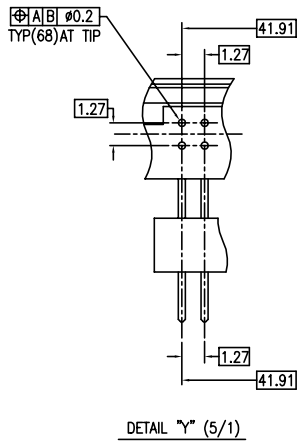
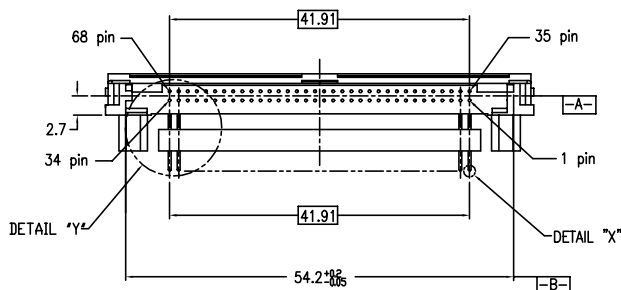
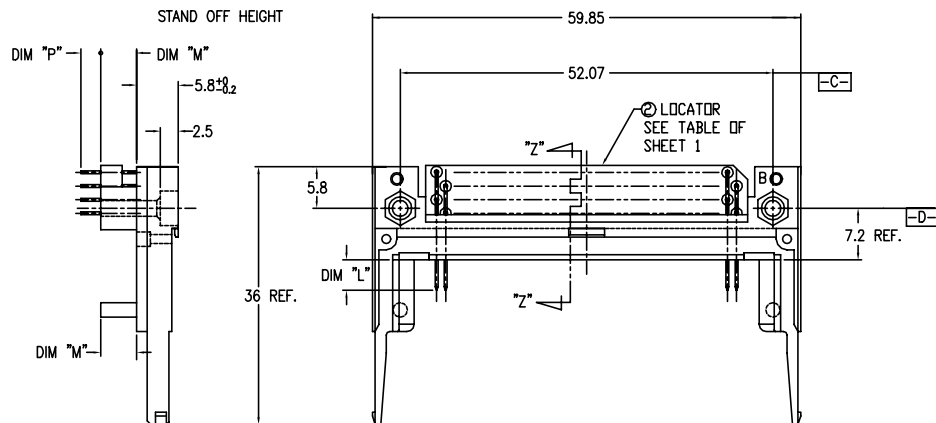
	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
PIN No.	OTHERS	36.67	1.17, 34, 35, 51, 68

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008



mat'l. code	surface	tolerance	projection	product family
ISO 1302	ISO 406	ISO 1101	PCMCIA	
tr/ecn nldr	date	tolerances unless otherwise specified	title	
AJ		angles	linear	68 PDS EJECTOR HEADER ASSY FOR 5.0V APPLICATION
		±2'	scale	
	dr Mike Zhou	11/21/06		dwg no 92140 sheet 5 of 7 size A4
	enr Mike Zhou	11/21/06		
	chr David Qin	11/21/06		
	app Jack Wang	11/21/06		
sheet	revision			type CUSTOMER Drawing
index	sheet			

PRODUCT NO.		
92140-510	92140-510LF Δ	92140-510HLF Δ
92140-520	92140-520LF Δ	
92140-520H Δ	92140-520HLF Δ	
92140-530	92140-530LF Δ	
92140-540	92140-540LF Δ	



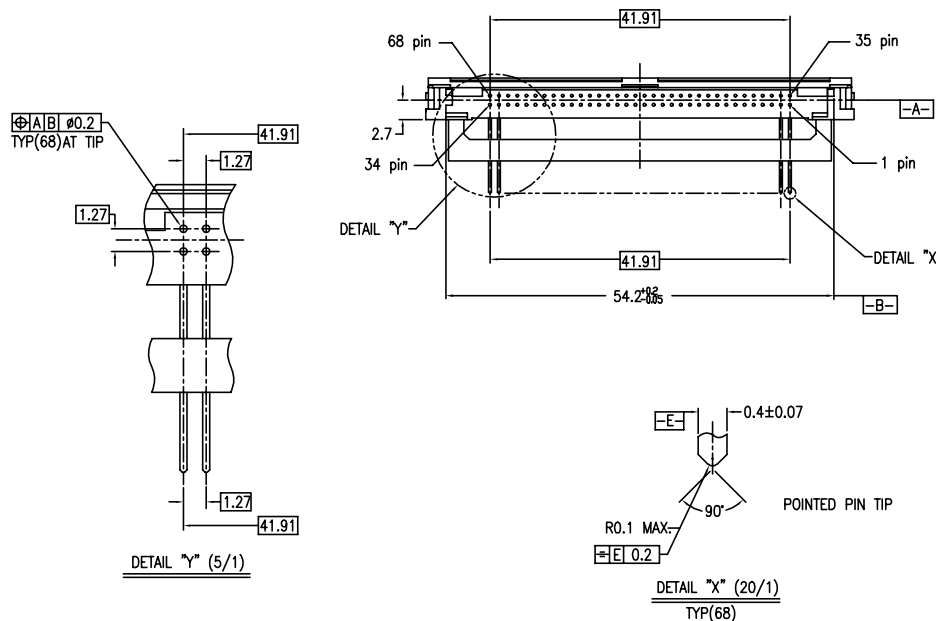
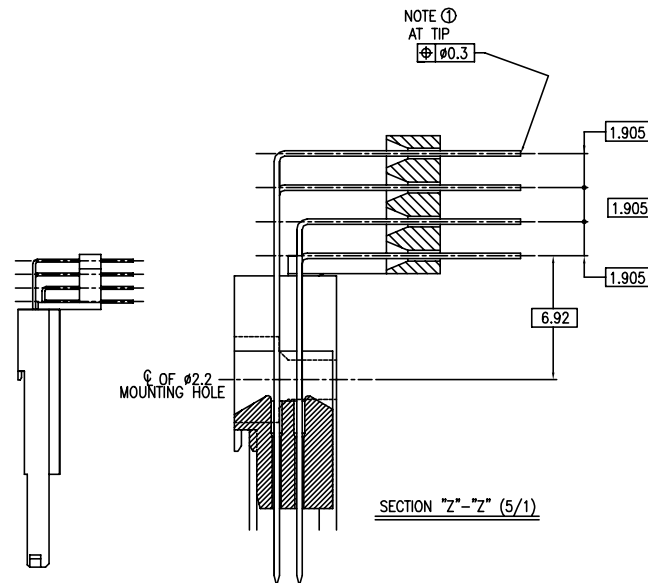
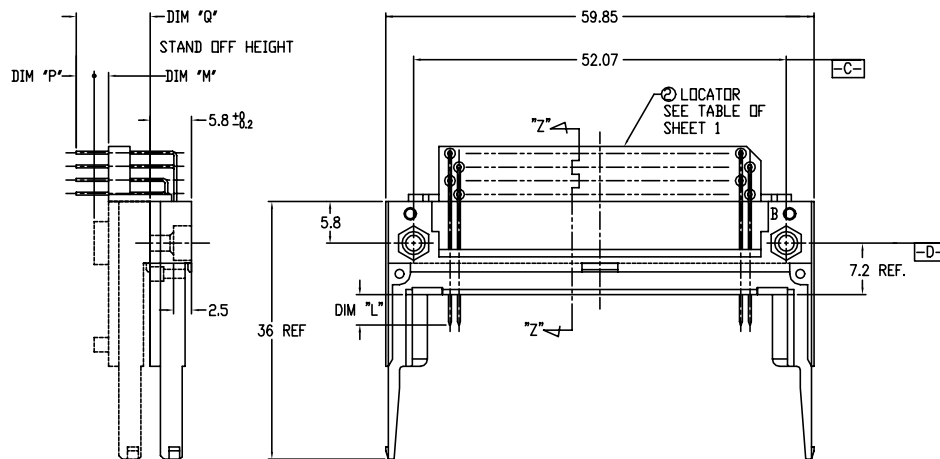
- NOTES
- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 - SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL
HOUSING : LCP UL94V-0 NATURAL.
PIN : PHOSPHOR BRONZE.
HOLD-DOWN : BRASS
 - FINISH
PIN
UNDER PLATING : 0.5µm MIN. Ni.
CONTACT AREA : 0.1µm MIN. Au.
OVER 0.5µm MIN. Pb-Ni.
SOLDER AREA : 2.5µm MIN. Sn-Pb.
OR 2.5µm MIN. PURE Sn.(FOR -XXXLF)
HOLD-DOWN : 2.5µm MIN. PURE Sn.
 - RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
 - SEQUENCE PIN ASSIGNMENT

PIN No.	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
OTHERS	36.67	117.34	36.67

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008

mat'l. code				surface		tolerance		projection		product family PCMCIA									
				ISO 1302		ISO 406 ISO 1101													
ltr		ecn no		dr		date		tolerances unless otherwise specified		title									
AJ										68 PDS EJECTOR HEADER ASSY									
								angles		FOR 5.0V APPLICATION									
								linear											
								±0.3											
								±2'		scale									
								dr		dwg no									
								Mike Zhou		sheet6 of 7 size									
								11/21/06		92140									
								engr		A4									
								Mike Zhou											
								11/21/06											
								chr											
								David Qin											
								11/21/06											
								app											
								Jack Wang											
								11/21/06											
sheet		revision								type CUSTOMER Drawing									
index		sheet																	

PRODUCT NO.
92140-505
92140-505LF
92140-535
92140-535LF
92140-545
92140-545LF



- NOTES
- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 - SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL
HOUSING : LCP UL94V-0 NATURAL.
PIN : PHOSPHOR BRONZE.
HOLD-DOWN : BRASS
 - FINISH
PIN
UNDER PLATING : 0.5µm MIN. Ni.
CONTACT AREA : 0.1µm MIN. Au.
OVER 0.5µm MIN. Pb-Ni.
SOLDER AREA : 2.5µm MIN. Sn-Pb.
OR 2.5µm MIN. PURE Sn.(FOR -XXXLF)
HOLD-DOWN : 2.5µm MIN. PURE Sn.
 - RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
 - SEQUENCE PIN ASSIGNMENT

	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
PIN No.	1-734	36.67	35.51.68

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008

mat'l. code	surface	tolerance	projection	product family	PCMCIA
ISO 1302	ISO 406	ISO 1101	ISO 1101		
l'tecn nodr	date	tolerances unless otherwise specified	title	68 PDS EJECTOR HEADER ASSY FOR 5.0V APPLICATION	
AJ		angles	linear	dwg no	sheet 7 of 7
		±2'	±0.3	92140	size A4
	dr	Mike Zhou	11/21/06	type	CUSTOMER Drawing
	enr	Mike Zhou	11/21/06		
	chr	David Qin	11/21/06		
	app	Jack Wang	11/21/06		
sheet	revision				
index	sheet				