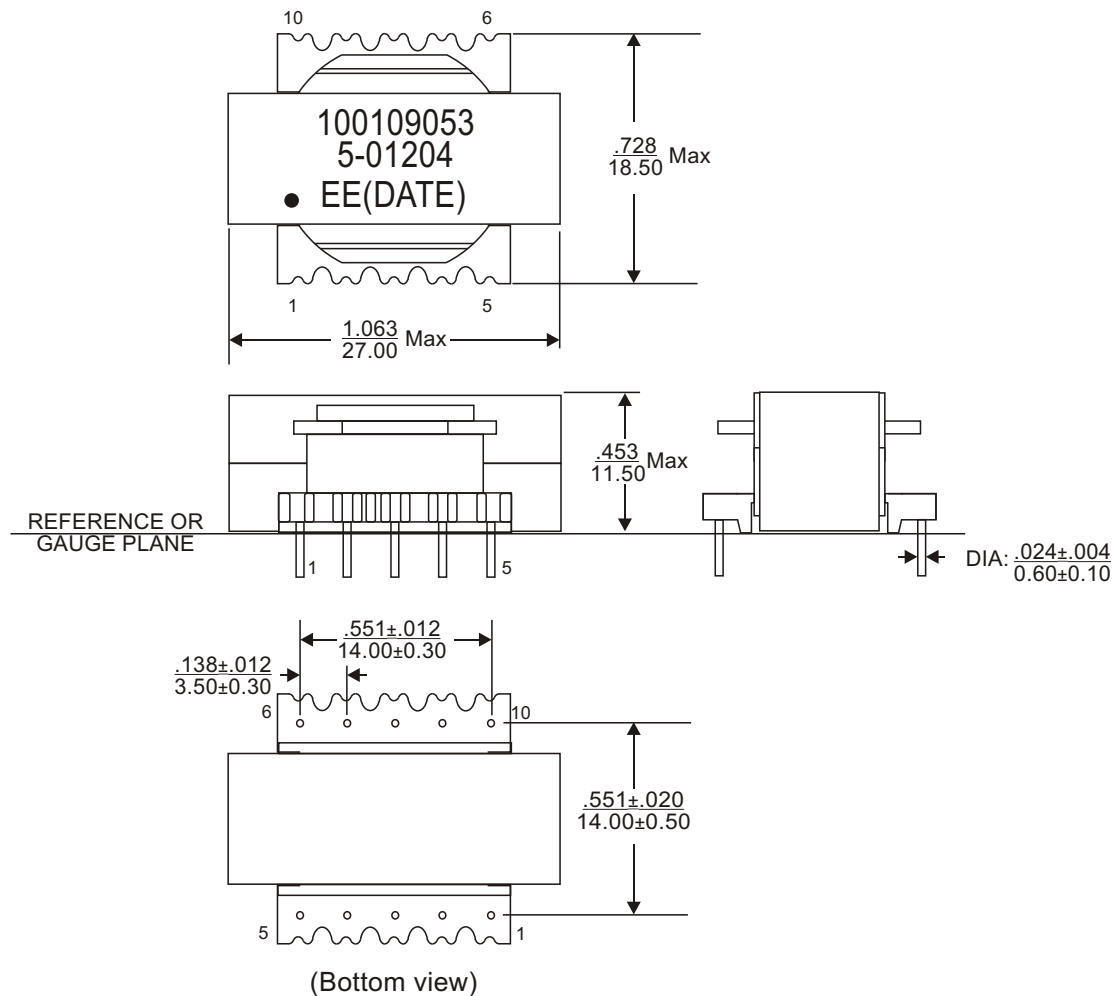


		REVISIONS												
		REV.	DESCRIPTION	ECN NO.	DATE									
		01	FIRST RELEASE FOR RFQ#A2013-12968	N/A	01/28/13									
		02	ADD CORE INFORMATION PER CUSTOMER'S REQUIREMENT	EE14426	02/01/13									
		PAGE 4 IS FOR INTERNAL USE ONLY												
PART NUMBER	PART DESCRIPTION			TITLE										
835-01204F	RoHS compliant per EU Directive 2011/65/EU			PXFm, ER2510, 1.3mH, TH, 10PIN										
WARNING ! ALL SHEETS OF THIS DOCUMENT ARE CONTROLLED DOCUMENTATION AND ARE NOT TO BE RELEASED OUTSIDE OF E&E OR ITS SUB-CONTRACTORS WITHOUT AUTHORIZATION.	UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCH/mm. TOLERANCE ARE: <table><tr><td>INCH</td><td>mm</td><td>ANGLE</td></tr><tr><td>.XXX .005 .XX .13 X.X 0.3</td><td></td><td></td></tr><tr><td>.XX .02 .X .5 X. 1</td><td></td><td></td></tr></table>	INCH	mm	ANGLE	.XXX .005 .XX .13 X.X 0.3			.XX .02 .X .5 X. 1			APPROVALS	DATE	E & E Magnetic Products Ltd.	
		INCH	mm	ANGLE										
		.XXX .005 .XX .13 X.X 0.3												
		.XX .02 .X .5 X. 1												
		DRAWN BY												
PROJ. ENG	J.L.XIONG	02/01/13		DRAWING NO./MODEL										
APPROVED BY	S.HONG	02/01/13		835-01204F										
Q.A.	J. YANG	02/01/13		REV										
	D.LUO	02/01/13		02										
				SCALE	PAGE 1 OF 4									
				DO NOT SCALE										



MECHANICAL OUTLINE



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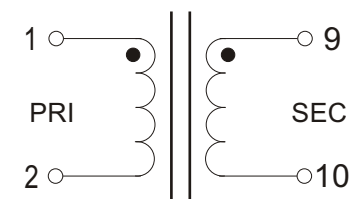
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1. Dimensions are specified in $\frac{\text{inches}}{\text{mm}}$ with higher precedence in mm.
2. Unless otherwise specified, all tolerance are $\frac{.010}{0.25}$.
3. "(DATE)" includes at least the manufacturing date code(in YYWW format).
4. Core material is PC40 or equivalent. Core gap is around 0.69mm.

ELECTRICAL SPECIFICATION @25 C:

PARAMETERS	UNIT	LIMITS
Turns Ratio(1-2):(9-10)	-	1 : 0.229 2%
Polarity	-	Per Schematic
Inductance,Ls(1-2)@1kHz,1Vrms	mH	1.3 10%
LL(1-2),short 9-10@100kHz,1Vrms	uH	1%*Ls normal and 30uH Max
HIPOT(1-2):(9-10)@10mA,1 minute	Vrms	500

5. Operating temperature range: -40 C to +125 C. The part temperature (ambient temperature + temperature rise) should not exceed the upper limit of the operating temperature under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.



SCHEMATIC



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