KA5S-SERIES

KA5S0765C/KA5S0965/KA5S12656/KA5S1265

Fairchild Power Switch(FPS)

Features

- Wide Operating Frequency Range Up to 150kHz
- Lowest Cost SMPS Solution
- Lowest External Components
- Low Start-up Current (Max:170µA)
- Low Operating Current (Max:12mA)
- Internal High Voltage SenseFET
- Over Voltage Protection With Latch Mode (Min23V)
- Over Load Protection With Latch Mode
- Over Current Protection With Latch Mode
- Internal Thermal Protection With Latch Mode
- Pulse By Pulse Over Current Limiting
- Under Voltage Lockout With Hysteresis
- External Sync. Terminal

TO-220-5L

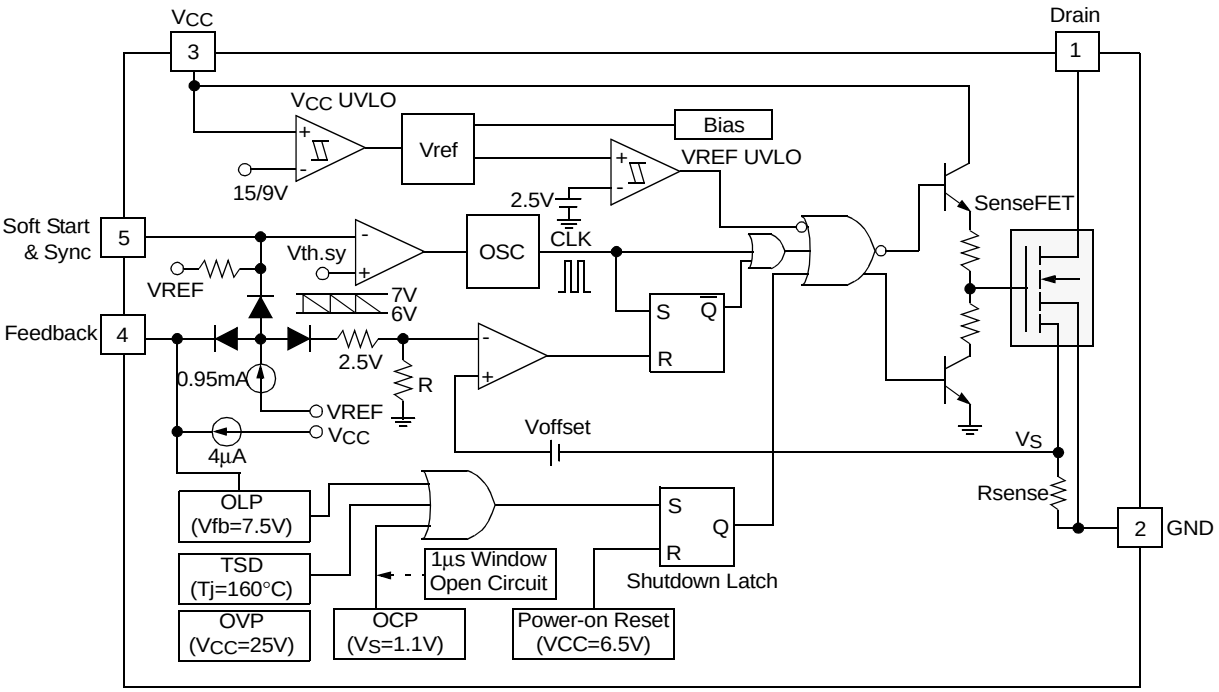


TO-3P-5L



1. Drain 2. Gnd 3. V_{CC} 4. FeedBack 5. Sync.

Internal Block Diagram



Rev.1.0.5

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Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
KA5S0765C			
Drain-Gate Voltage(R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-Source(GND) Voltage	V_{GS}	±30	V
Drain Current Pulsed[1]	I_{DM}	28	ADC
Continuous Drain Current (Tc = 25°C)	I_D	7.0	ADC
Continuous Drain Current (Tc = 100°C)	I_D	5.6	ADC
Single Pulsed Avalanch Current[3](Energy [2])	I_{AS}(E_{AS})	27(570)	A(mJ)
Maximum Supply Voltage	$V_{CC,MAX}$	30	V
Input Voltage Range	V_{FB}	-0.3 to V_{CC}	V
	V_{SS}	-0.3 to 8	V
Total Power Dissipation	P_D (Watt H/S)	140	W
	Darting	1.11	W/°C
Operating Junction Temperature.	T_J	+160	°C
Operating Ambient Temperature.	T_A	-25 to +85	°C
Storage Temperature Range.	T_{STG}	-55 to +150	°C

KA5S0965			
Drain-Gate Voltage(R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-Source(GND) Voltage	V_{GS}	±30	V
Drain Current Pulsed[1]	I_{DM}	36	ADC
Continuous Drain Current (Tc = 25°C)	I_D	9.0	ADC
Continuous Drain Current (Tc = 100°C)	I_D	5.8	ADC
Single Pulsed Avalanch Current[3](Energy [2])	I_{AS}(E_{AS})	25(950)	A(mJ)
Maximum Supply Voltage	$V_{CC,MAX}$	30	V
Input Voltage Range	V_{FB}	-0.3 to V_{CC}	V
	V_{SS}	-0.3 to 8	V
Total Power Dissipation	P_D (Watt H/S)	170	W
	Darting	1.33	W/°C
Operating Junction Temperature.	T_J	+160	°C
Operating Ambient Temperature.	T_A	-25 to +85	°C
Storage Temperature Range.	T_{STG}	-55 to +150	°C

Absolute Maximum Ratings (Continued)

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
KA5S12656			
Drain-Gate Voltage(R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-Source(GND) Voltage	V_{GS}	±30	V
Drain Current Pulsed(1)	I_{DM}	48	ADC
Continuous Drain Current (Tc = 25°C)	I_D	12	ADC
Continuous Drain Current (Tc = 100°C)	I_D	8.4	ADC
Single Pulsed Avalanch Current(3)(Energy (2))	I_{AS}(E_{AS})	25(785)	A(mJ)
Maximum Supply Voltage	$V_{CC,MAX}$	30	V
Input Voltage Range	V_{FB}	-0.3 to V_{CC}	V
	V_{SS}	-0.3 to 8	V
Total Power Dissipation	P_D (Watt H/S)	160	W
	Darting	1.28	W/°C
Operating Junction Temperature.	T_J	+160	°C
Operating Ambient Temperature.	T_A	-25 to +85	°C
Storage Temperature Range.	T_{STG}	-55 to +150	°C

KA5S1265			
Drain-Gate Voltage(R_{GS}=1MΩ)	V_{DGR}	650	V
Gate-Source(GND) Voltage	V_{GS}	±30	V
Drain Current Pulsed(1)	I_{DM}	48	ADC
Continuous Drain Current (Tc = 25°C)	I_D	12	ADC
Continuous Drain Current (Tc = 100°C)	I_D	8.4	ADC
Single Pulsed Avalanch Current(3)(Energy (2))	I_{AS}(E_{AS})	42(785)	A(mJ)
Maximum Supply Voltage	$V_{CC,MAX}$	30	V
Input Voltage Range	V_{FB}	-0.3 to V_{CC}	V
	V_{SS}	-0.3 to 8	V
Total Power Dissipation	P_D (Watt H/S)	160	W
	Darting	1.28	W/°C
Operating Junction Temperature.	T_J	+160	°C
Operating Ambient Temperature.	T_A	-25 to +85	°C
Storage Temperature Range.	T_{STG}	-55 to +150	°C

Note:

1. Repetitive rating : Pulse width limited by maximum junction temperature
2. L = 10mH, V_{DD} =50V, R_G = 27Ω, starting T_j = 25°C
3. L = 13μH, starting T_j = 25°C

Electrical Characteristics (SFET Part)

(Ta = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
KA5S0765C						
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	μA
Static Drain-Source on Resistance(1)	RDS(on)	VGS=10V, ID=4.0A	-	1.25	1.6	Ω
Forward Transconductance(1)	gfs	VDS=15V, ID=4.0A	3.0	-	-	S
Input Capacitance	Ciss	VGS=0V, VDS=25V, f = 1MHz	-	1600	-	pF
Output Capacitance	Coss		-	310	-	
Reverse Transfer Capacitance	Crss		-	120	-	
Turn on Delay Time	td(on)	VDD=0.5B VDSS, ID=7.0A (MOSFET switching time is essentially independent of operating temperature)	-	25	-	nS
Rise Time	tr		-	55	-	
Turn Off Delay Time	td(off)		-	80	-	
Fall Time	tf		-	50	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=7.0A, VDS=0.5B VDSS(MOSFET switching time is essentially independent of operating temperature)	-	-	72	nC
Gate-Source Charge	Qgs		-	9.3	-	
Gate-Drain (Miller) Charge	Qgd		-	29.3	-	
KA5S0965						
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	μA
Static Drain-Source on Resistance(1)	RDS(on)	VGS=10V, ID=4.5A	-	0.96	1.2	Ω
Forward Transconductance(1)	gfs	VDS=50V, ID=4.5A	5.0	-	-	S
Input Capacitance	Ciss	VGS=0V, VDS=25V, f = 1MHz	-	1750	-	pF
Output Capacitance	Coss		-	190	-	
Reverse Transfer Capacitance	Crss		-	78	-	
Turn on Delay Time	td(on)	VDD=0.5B VDSS, ID=9.0A (MOSFET switching time is essentially independent of operating temperature)	-	20	50	nS
Rise Time	tr		-	23	55	
Turn Off Delay Time	td(off)		-	85	180	
Fall Time	tf		-	30	70	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=9.0A, VDS=0.5B VDSS(MOSFET switching time is essentially independent of operating temperature)	-	74	95	nC
Gate-Source Charge	Qgs		-	12	-	
Gate-Drain (Miller) Charge	Qgd		-	35	-	

Electrical Characteristics (SFET Part) (Continued)

(Ta = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
KA5S12656						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=50μA	650	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=Max., Rating, V_{GS}=0V	-	-	50	μA
		V_{DS}=0.8Max., Rating, V_{GS}=0V, T_C=125°C	-	-	200	μA
Static Drain-Source On Resistance(1)	R_{DS}(on)	V_{GS}=10V, I_D=6.0A	-	0.72	0.9	Ω
Forward Transconductance(1)	gfs	V_{DS}=50V, I_D=4.0A	5.7	-	-	S
Input Capacitance	Ciss	V_{GS}=0V, V_{DS}=25V, f = 1MHz	-	2700	-	pF
Output Capacitance	Coss		-	300	-	
Reverse Transfer Capacitance	Crss		-	61	-	
Turn on Delay Time	td(on)	V_{DD}=0.5B V_{DSS}, I_D=12.0A (MOSFET switching time is essentially independent of operating temperature)	-	18	-	nS
Rise Time	tr		-	37	-	
Turn Off Delay Time	td(off)		-	88	-	
Fall Time	tf		-	36	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	V_{GS}=10V, I_D=12.0A, V_{DS}=0.5B V_{DSS}(MOSFET switching time is essentially independent of operating temperature)	-	-	140	nC
Gate-Source Charge	Qgs		-	20	-	
Gate-Drain (Miller) Charge	Qgd		-	69	-	
KA5S1265						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=50μA	650	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=Max., Rating, V_{GS}=0V	-	-	50	μA
		V_{DS}=0.8Max., Rating, V_{GS}=0V, T_C=125°C	-	-	200	μA
Static Drain-Source on Resistance(1)	R_{DS}(on)	V_{GS}=10V, I_D=6.0A	-	0.72	0.9	Ω
Forward Transconductance(1)	gfs	V_{DS}=50V, I_D=4.0A	5.7	-	-	S
Input Capacitance	Ciss	V_{GS}=0V, V_{DS}=25V, f = 1MHz	-	2700	-	pF
Output Capacitance	Coss		-	300	-	
Reverse Transfer Capacitance	Crss		-	61	-	
Turn on Delay Time	td(on)	V_{DD}=0.5BV_{DSS}, I_D=12.0A (MOSFET switching time is essentially independent of operating temperature)	-	18	-	nS
Rise Time	tr		-	37	-	
Turn Off Delay Time	td(off)		-	88	-	
Fall Time	tf		-	36	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	V_{GS}=10V, I_D=12.0A, V_{DS}=0.5B V_{DSS}(MOSFET switching time is essentially independent of operating temperature)	-	-	140	nC
Gate-Source Charge	Qgs		-	20	-	
Gate-Drain (Miller) Charge	Qgd		-	69	-	

Note:

1. Pulse Test : Pulse width ≤ 300uS, Duty Cycle ≤ 2%

2.MOSFET switching time is essentially independent of operating temperature

3. $S = \frac{1}{R}$

Electrical Characteristics (Control Part) (Continued)

(V_{CC}=16V, Tamb = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start Threshold Voltage	V_{START}	V_{FB}=GND	14	15	16	V
Stop Threshold Voltage	V_{STOP}	V_{FB}=GND	8	9	10	V
OSCILLATOR SECTION						
Initial Frequency	F_{OSC}	-	18	20	22	kHz
Voltage Stability	F_{STABLE}	12V ≤ V_{CC} ≤ 23V	0	1	3	%
Temperature Stability (Note2)	ΔF_{OSC}	-25°C ≤ Ta≤ 85°C	0	±5	±10	%
Maximum Duty Cycle	D_{MAX}	-	92	95	98	%
Minimum Duty Cycle	D_{MIN}	-	-	-	0	%
FEEDBACK SECTION						
Feedback Source Current	I_{FB}	V_{FB}=GND	0.7	0.9	1.1	mA
Shutdown Feedback Voltage	V_{SD}	V_{FB} ≥ 6.9V	6.9	7.5	8.1	V
Shutdown Delay Current	I_{DELAY}	V_{FB}=5V	3.0	4.0	5.0	μA
SYNC. & SOFTSTART SECTION						
Softstart Voltage	V_{SS}	V_{FB}=2V	4.7	5.0	5.3	V
Softstart Current	I_{SS}	V_{SS}=0V	0.75	0.95	1.15	mA
Sync High Threshold Voltage(Note3)	V_{SYNCH}	V_{CC}=16V , V_{FB}=5V	-	7.0	-	V
Sync Low Threshold Voltage(Note3)	V_{SYNCL}	V_{CC}=16V , V_{FB}=5V	-	6.0	-	V
CURRENT LIMIT(SELF-PROTECTION)SECTION						
Peak Current Limit (Note4)	I_{OVER}	**KA5S0765C**	3.52	4.0	4.48	A
		KA5S0965	5.28	6.0	6.72	
		KA5S12656	5.28	6.0	6.72	
		KA5S1265	7.04	8.0	8.96	
PROTECTION SECTION						
Over Voltage Protection	V_{OVP}	V_{CC} ≥ 24V	23	25	28	V
Over Current Latch voltage(Note3)	V_{OCL}	-	0.9	1.0	1.1	V
Thermal Shutdown Tempature(Note2)	TSD	-	140	160	-	°C
TOTAL DEVICE SECTION						
Start Up Current	I_{START}	V_{FB}=GND, V_{CC}=14V	-	0.1	0.17	mA
Operating Supply Current(Note1)	I_{OP}	V_{FB}=GND, V_{CC}=16V	-	7	12	mA
	$I_{OP(MIN)}$	V_{FB}=GND, V_{CC}=12V				
	$I_{OP(MAX)}$	V_{FB}=GND, V_{CC}=30V				

Note:

1. These parameters are the current flowing in the control IC.
2. These parameters, although guaranteed, are not 100% tested in mass production
3. These parameters, although guaranteed, are tested in EDS(wafer test) process
4. These parameters are indicated Inductor current.

Typical Performance Characteristics

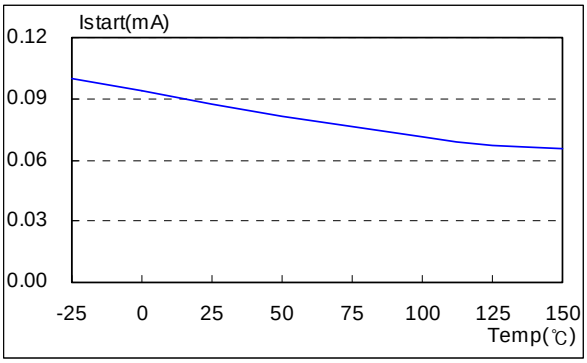


Figure 1. Start Up Current vs. Temp.

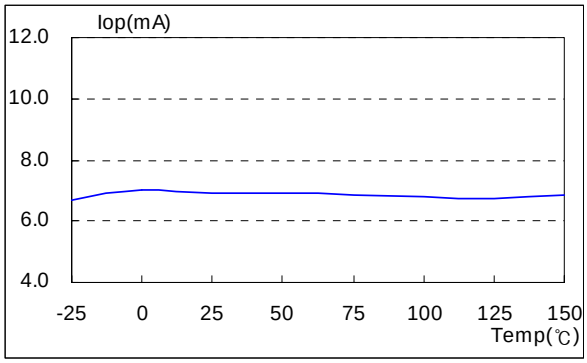


Figure 2. Operating Supply Current vs. Temp.

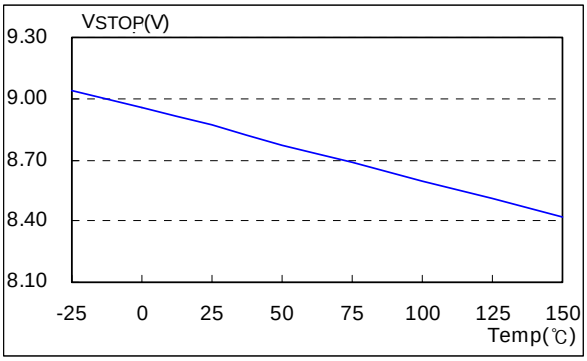


Figure 3. Stop Threshold Voltage vs. Temp

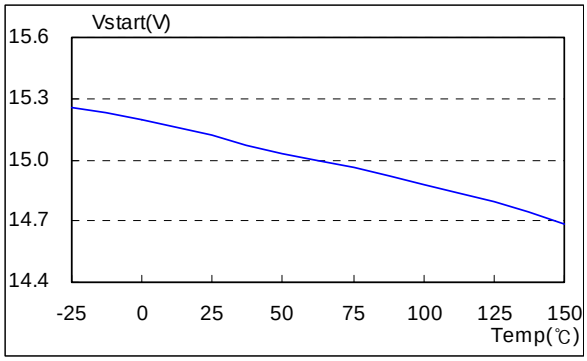


Figure 4. Start Threshold Voltage vs. Temp

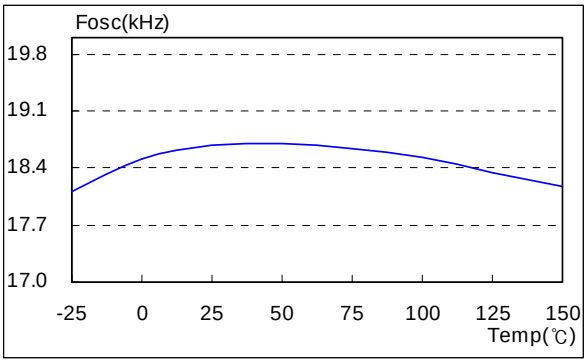


Figure 5. Initial Frequency VS. Temp

Typical Performance Characteristics (Continued)

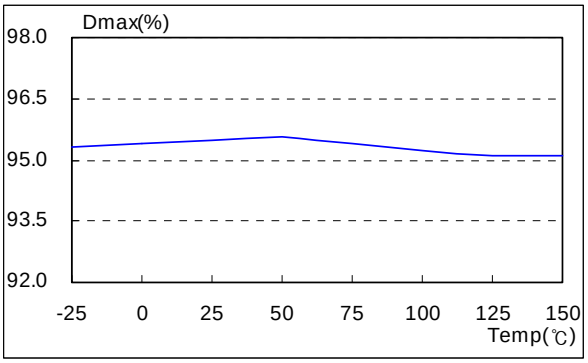


Figure 6. Maximum Duty vs. Temp.

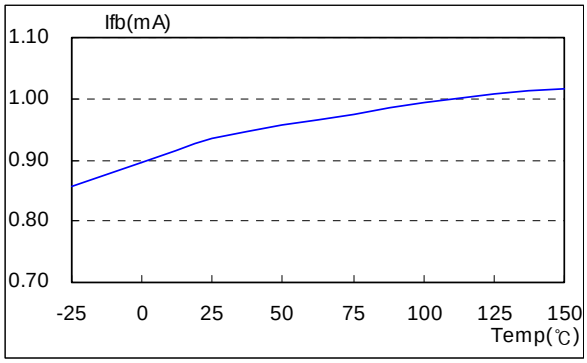


Figure 7. Feedback Source Current vs. Temp.

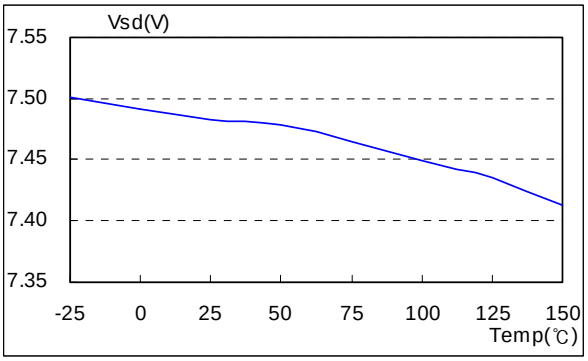


Figure 8. Shutdown Feedback Voltage vs. Temp.

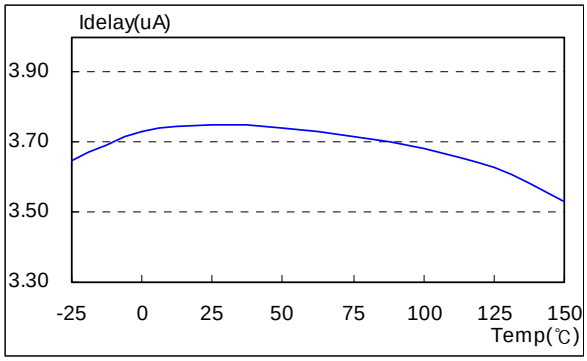


Figure 9. Shutdown Delay Current vs. Temp.

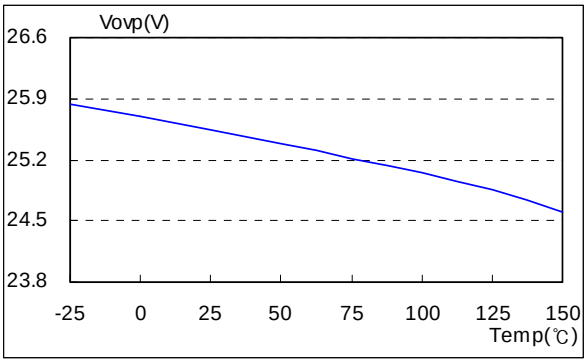


Figure 10. Over Voltage Protection vs. Temp.

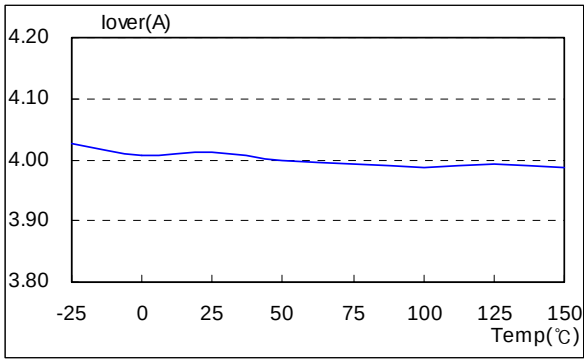


Figure 11. Peak Current Limit vs. Temp

Typical Performance Characteristics (Continued)

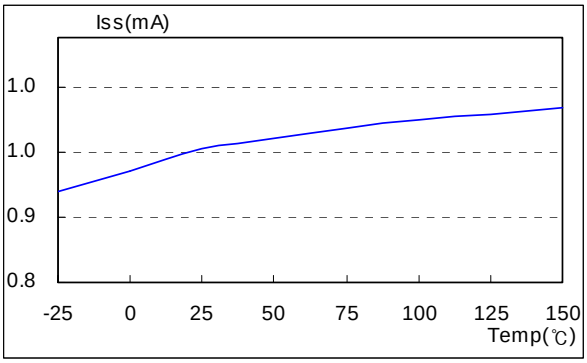


Figure 12. Soft Start Current vs. Temp.

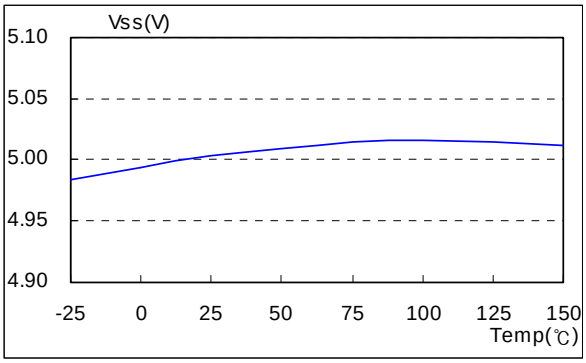
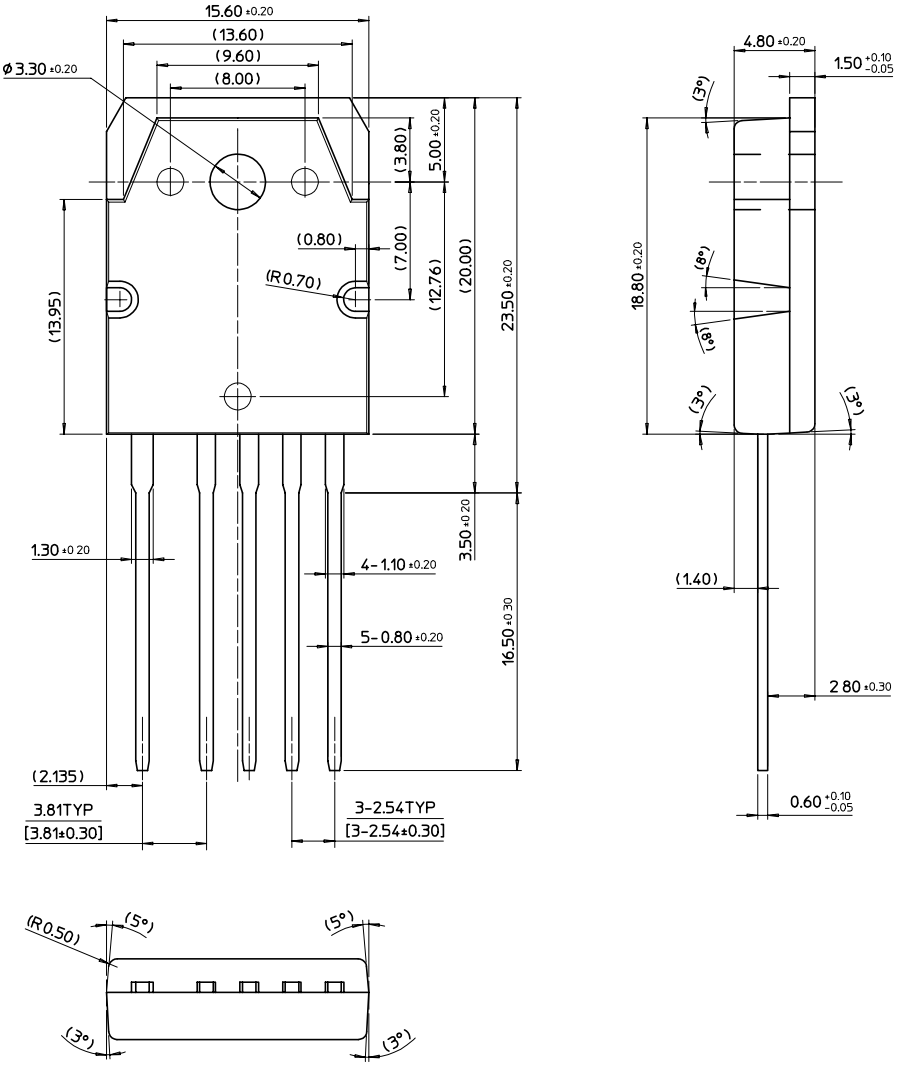


Figure 13. Soft Start Voltage vs. Temp.

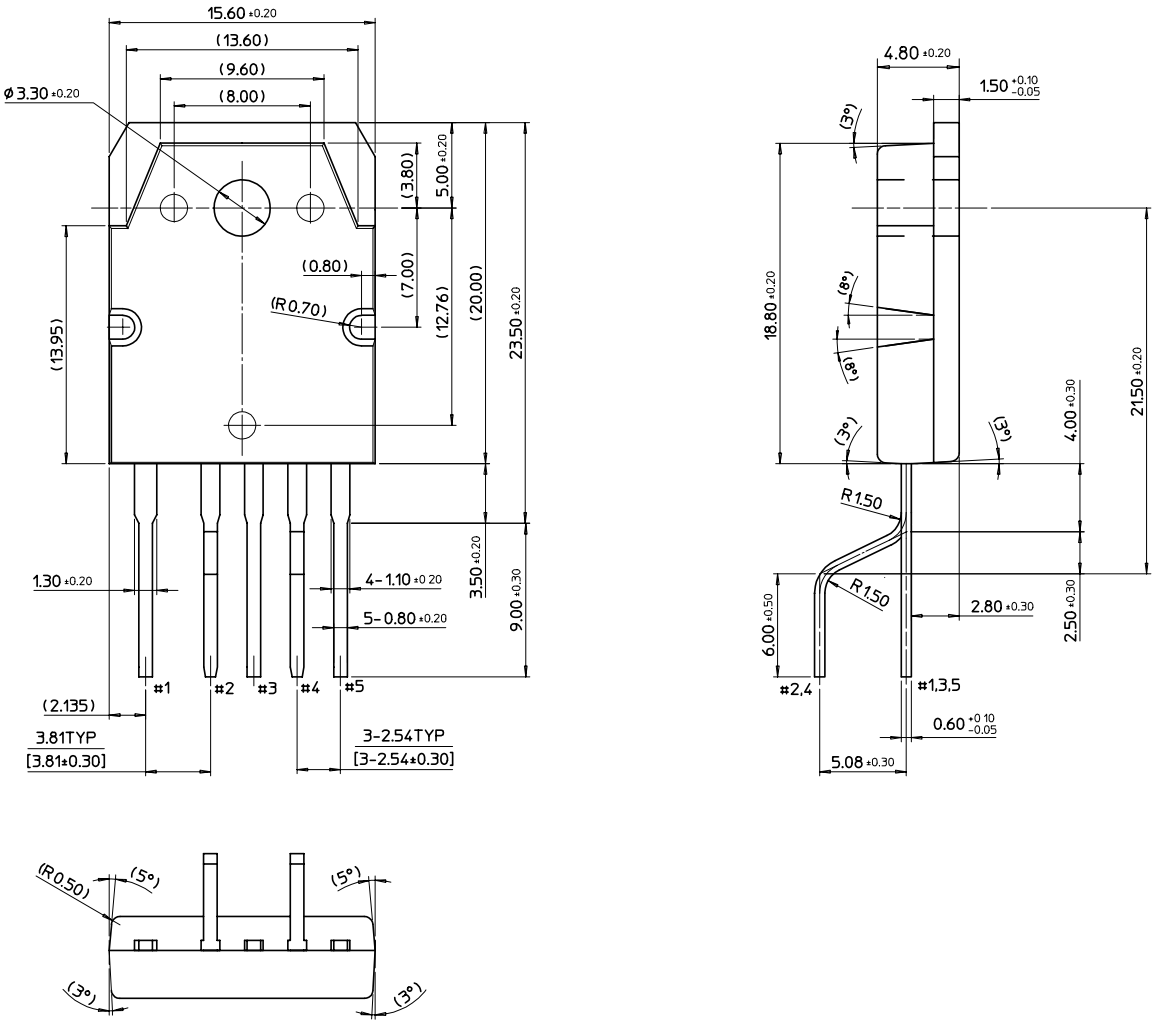
Package Dimensions

TO-3P-5L



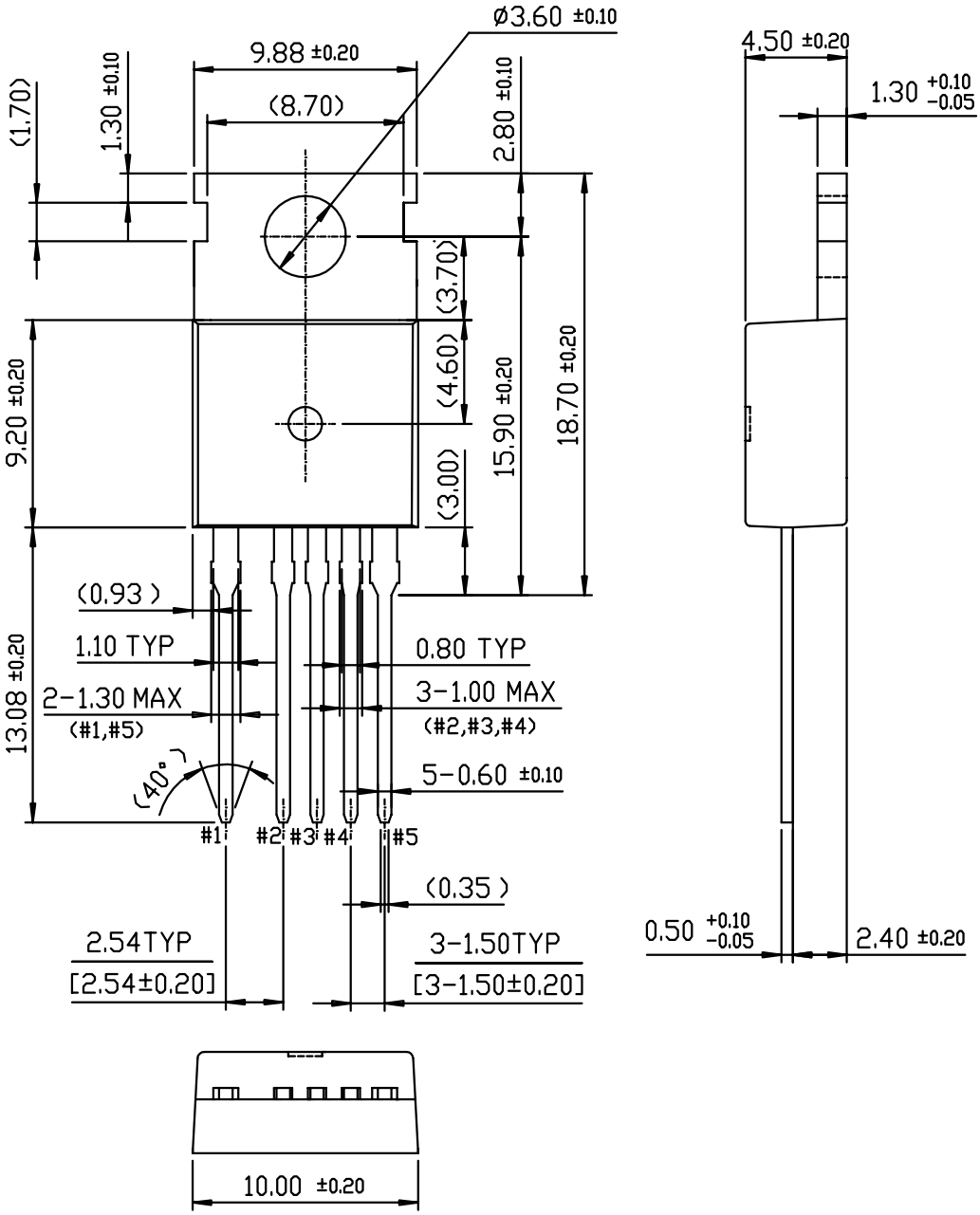
Package Dimensions (Continued)

TO-3P-5L(Forming)



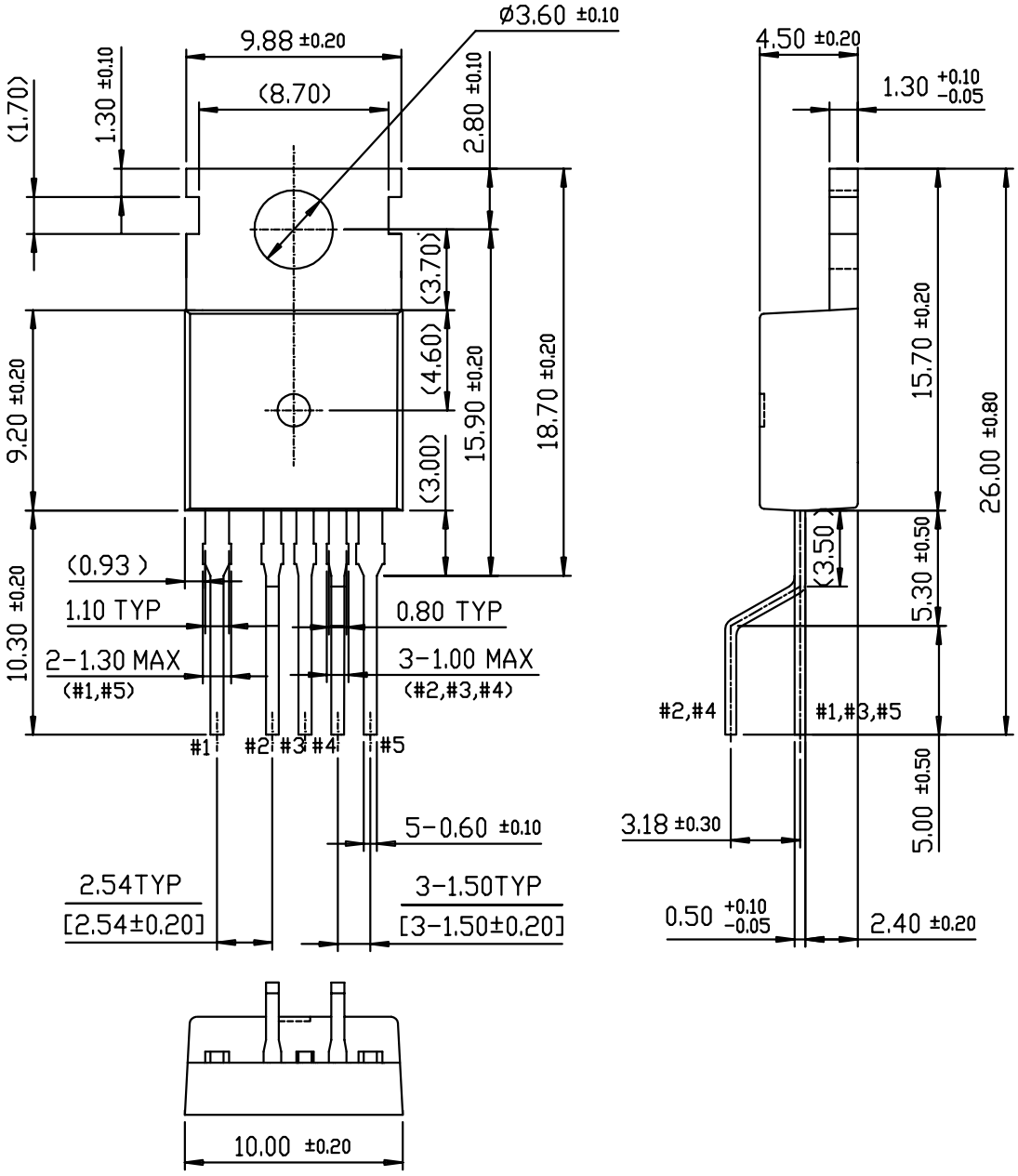
Package Dimensions (Continued)

TO-220-5L

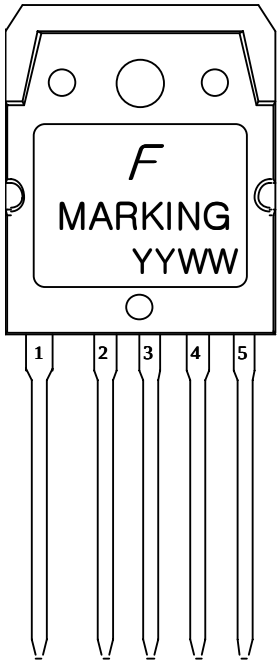


Package Dimensions (Continued)

TO-220-5L(Forming)



TOP Mark and Pinout Information



Pin No.	Symbol	Description
1	Drain	SenseFET Drain
2	GND	Ground (Source)
3	V_{CC}	Control Part Supply Input
4	F/B	PWM Non Inverting Input
5	S/S	Soft start & External Sync.

Device	Marking
KA5S0765C	5S0765C
KA5S0965	5S0965
KA5S12656	5S12656
KA5S1265	5S1265

Notes ;
(1) F ; Fairchild Semiconductor
(2) 5S0765C, 5S0965, 5S12656, 5S1265; Device Marking Name
(3) YY: Last Two Digit of Calender Year
(4) WW: Patweek Based on Fairchild Semiconductor Work Month Calender

Ordering Information

Product Number	Package	Marking Code	BVdss	Rds(on)
KA5S0765CTU	TO-220-5L	5S0765C	650V	1.6Ω
KA5S0765CYDTU	TO-220-5L(Forming)			
KA5S0965TU	TO-3P-5L	5S0965	650V	1.2Ω
KA5S0965YDTU	TO-3P-5L(Forming)			
KA5S12656TU	TO-3P-5L	5S12656	650V	0.9Ω
KA5S12656YDTU	TO-3P-5L(Forming)			
KA5S1265TU	TO-3P-5L	5S1265	650V	0.9Ω
KA5S1265YDTU	TO-3P-5L(Forming)			

TU : Non Forming Type
YDTU : Forming Type

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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