Ordering number : ENN5321A

NPN Triple Diffused Planar Silicon Transistor

2SC5265LS

Inverter-Controlled Lighting Applications

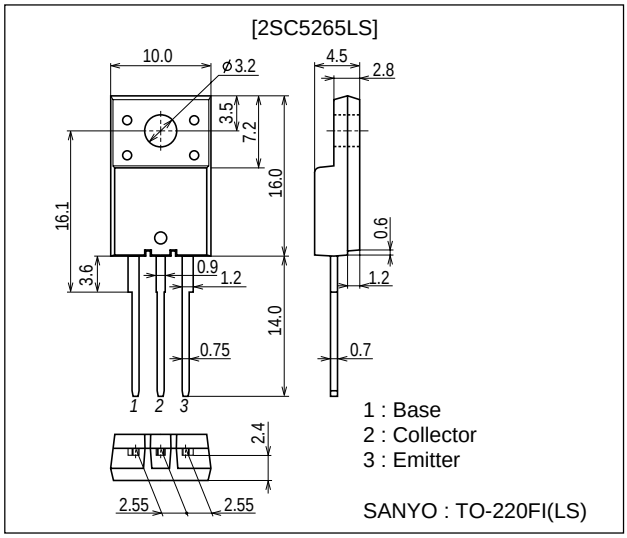
Features

- High breakdown voltage(V_{CBO}=1200V).
- High reliability(Adoption of HVP process).
- Adoption of MBIT process.

Package Dimensions

unit : mm

2079D



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		1200	V
Collector-to-Emitter Voltage	V_{CEO}		600	V
Emitter-to-Base Voltage	V_{EBO}		9	V
Collector Current	I_C		4	A
Collector Current (Pulse)	I_{CP}		8	A
Collector Dissipation	P_C		2	W
		Tc=25°C	30	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		−55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I_{CBO}	V_{CB}=600V, I_E=0			10	μA
	I_{CES}	V_{CE}=1200V, R_{BE}=0			1.0	mA
Collector-to-Emitter Sustain Voltage	V_{CEO}(sus)	I_C=100mA, I_B=0	600			V
Emitter Cutoff Current	I_{EBO}	V_{EB}=9V, I_C=0			1.0	mA

Continued on next page.

■ Any and all SANYO products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.

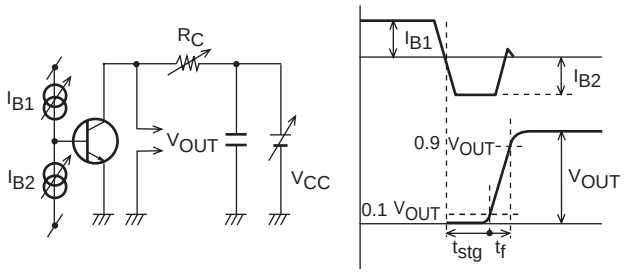
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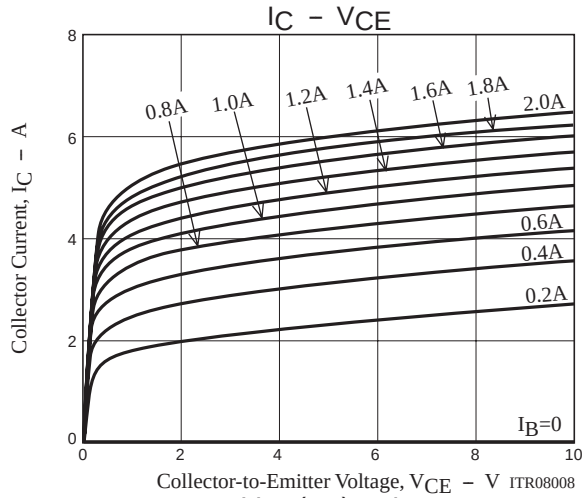
SANYO Electric Co.,Ltd. Semiconductor Company
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

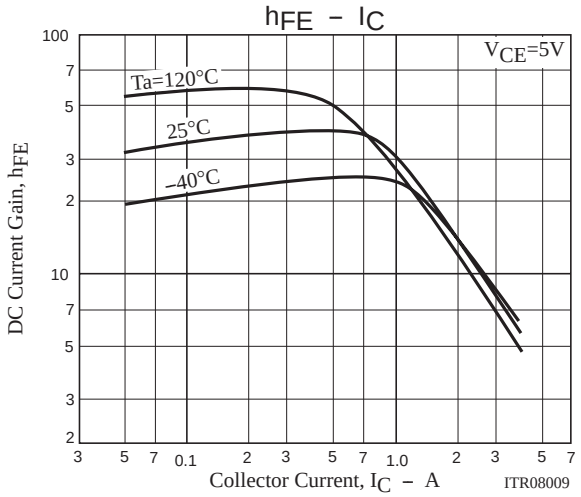
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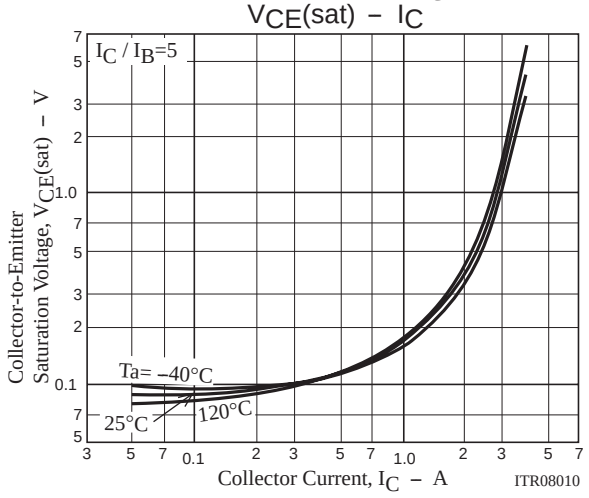
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Emitter Saturation Voltage	V_{CE}(sat)	I_C=2.0A, I_B=0.4A			1.0	V
Base-to-Emitter Saturation Voltage	V_{BE}(sat)	I_C=2.0A, I_B=0.4A			1.5	V
DC Current Gain	$h_{FE}1$	V_{CE}=5V, I_C=0.3A	30	40	50	
	$h_{FE}2$	V_{CE}=5V, I_C=1.5A	10			
Storage Time	t_{stg}	I_C=2.0A, I_{B1}=0.4A, I_{B2}=−0.8A			2.5	μs
Fall Time	t_f	I_C=2.0A, I_{B1}=0.4A, I_{B2}=−0.8A			0.15	μs

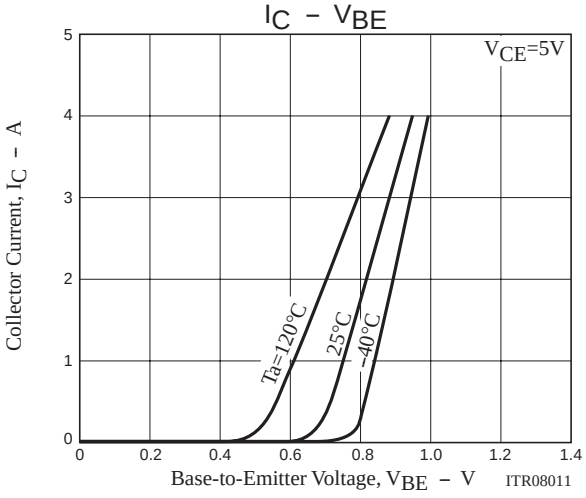
Switching Time Test Circuit



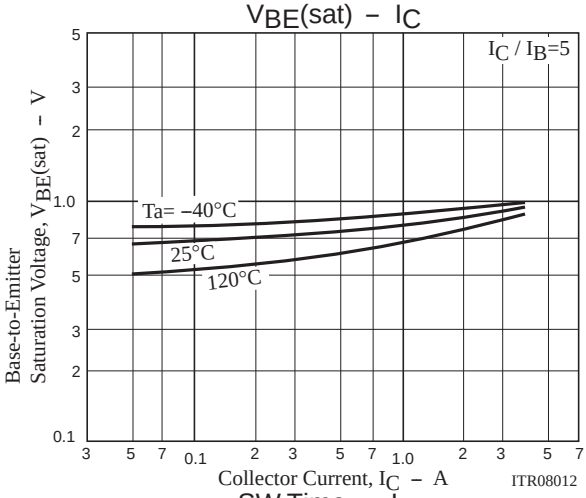




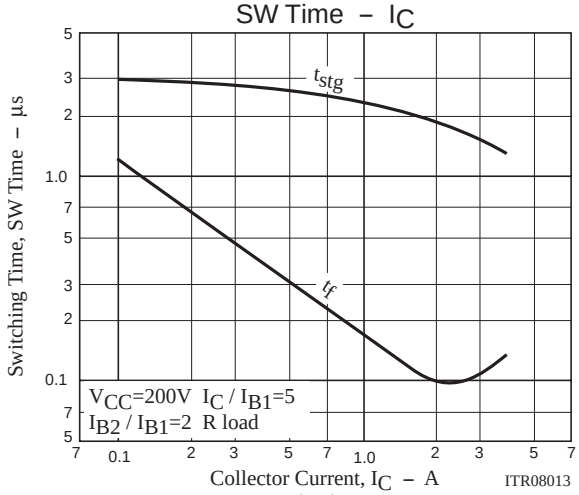




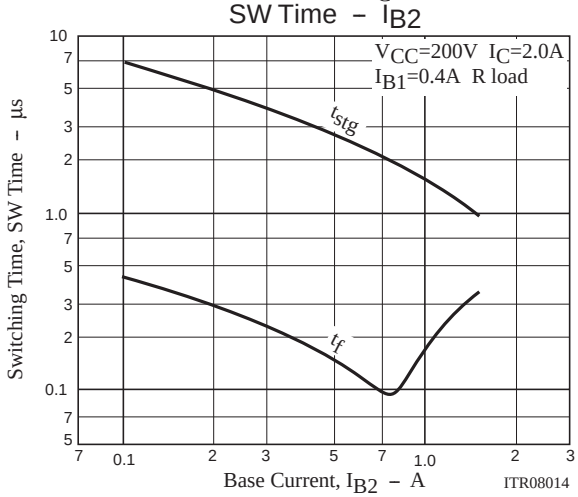
$V_{BE}(sat)$ – I_C



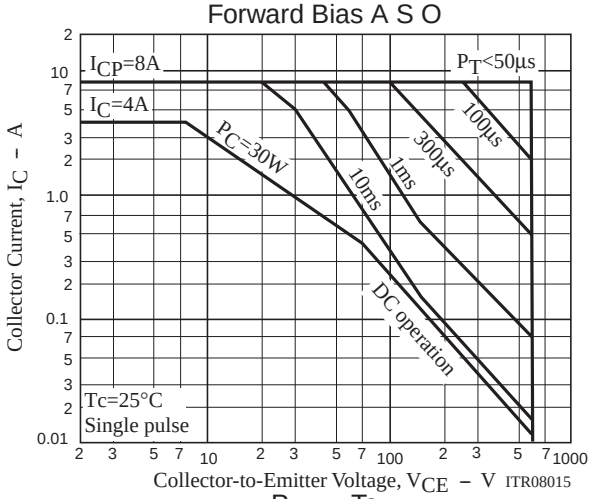
SW Time – I_C



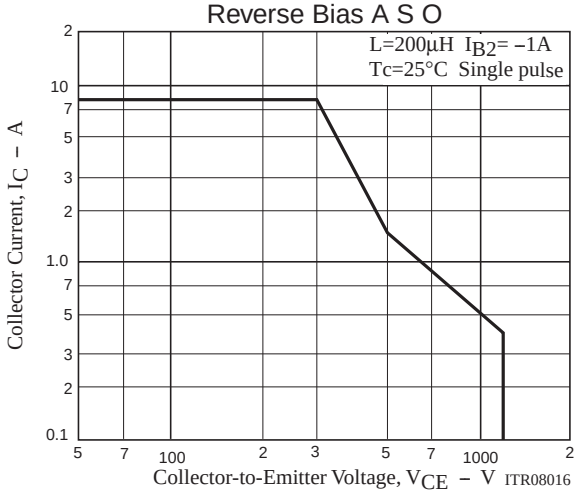
SW Time – I_{B2}



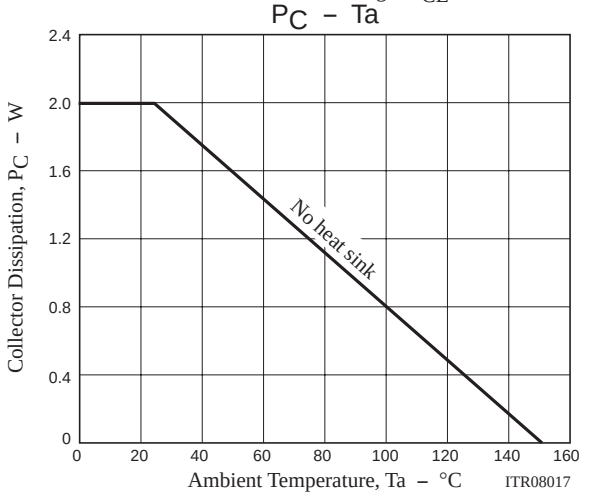
Forward Bias A S O



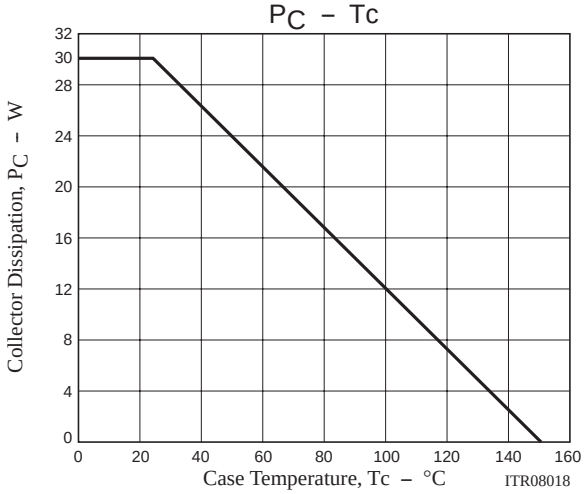
Reverse Bias A S O



P_C – Ta



P_C – Tc



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