

Silicon NPN Power Transistors

2SC3280

DESCRIPTION

- With TO-3PL package
- Complement to type 2SA1301

APPLICATIONS

- Power amplifier applications
- Recommended for 80W high fidelity audio frequency amplifier output stage

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

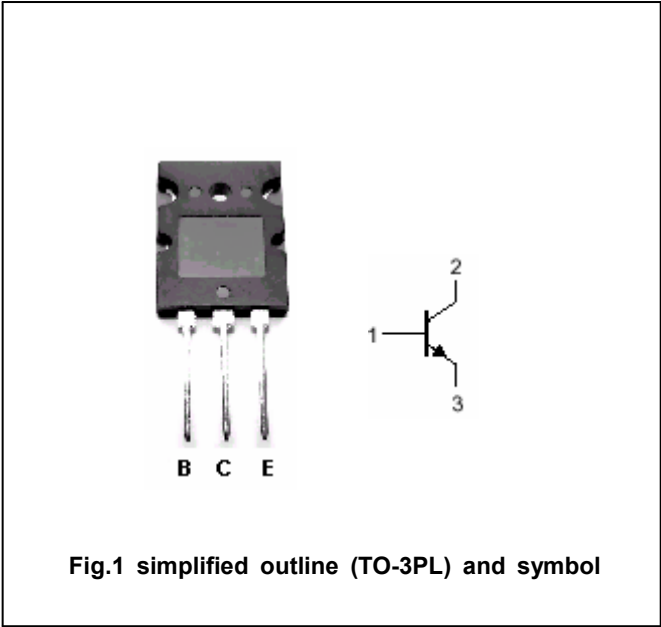


Fig.1 simplified outline (TO-3PL) and symbol

Absolute maximum ratings(Ta=25℃)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	160	V
V _{CEO}	Collector-emitter voltage	Open base	160	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		12	A
I _B	Base current		1.2	A
P _C	Collector power dissipation	T _C =25℃	120	W
T _j	Junction temperature		150	℃
T _{stg}	Storage temperature		-55~150	℃

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CHARACTERISTICS

T_j=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =50mA ; I _B =0	160			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =8A I _B =0.8A			2.5	V
V _{BE}	Base-emitter voltage	I _C =6A ; V _{CE} =5V			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =160V I _E =0			5	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			5	μA
h _{FE-1}	DC current gain	I _C =1A ; V _{CE} =5V	55		160	
h _{FE-2}	DC current gain	I _C =6A ; V _{CE} =5V	35			
f _T	Transition frequency	I _C =1A ; V _{CE} =5V		30		MHz
C _{OB}	Collector output capacitance	f=1MHz; V _{CB} =10V		220		pF

◆ h_{FE-1} classifications

R	O
55-110	80-160

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PACKAGE OUTLINE

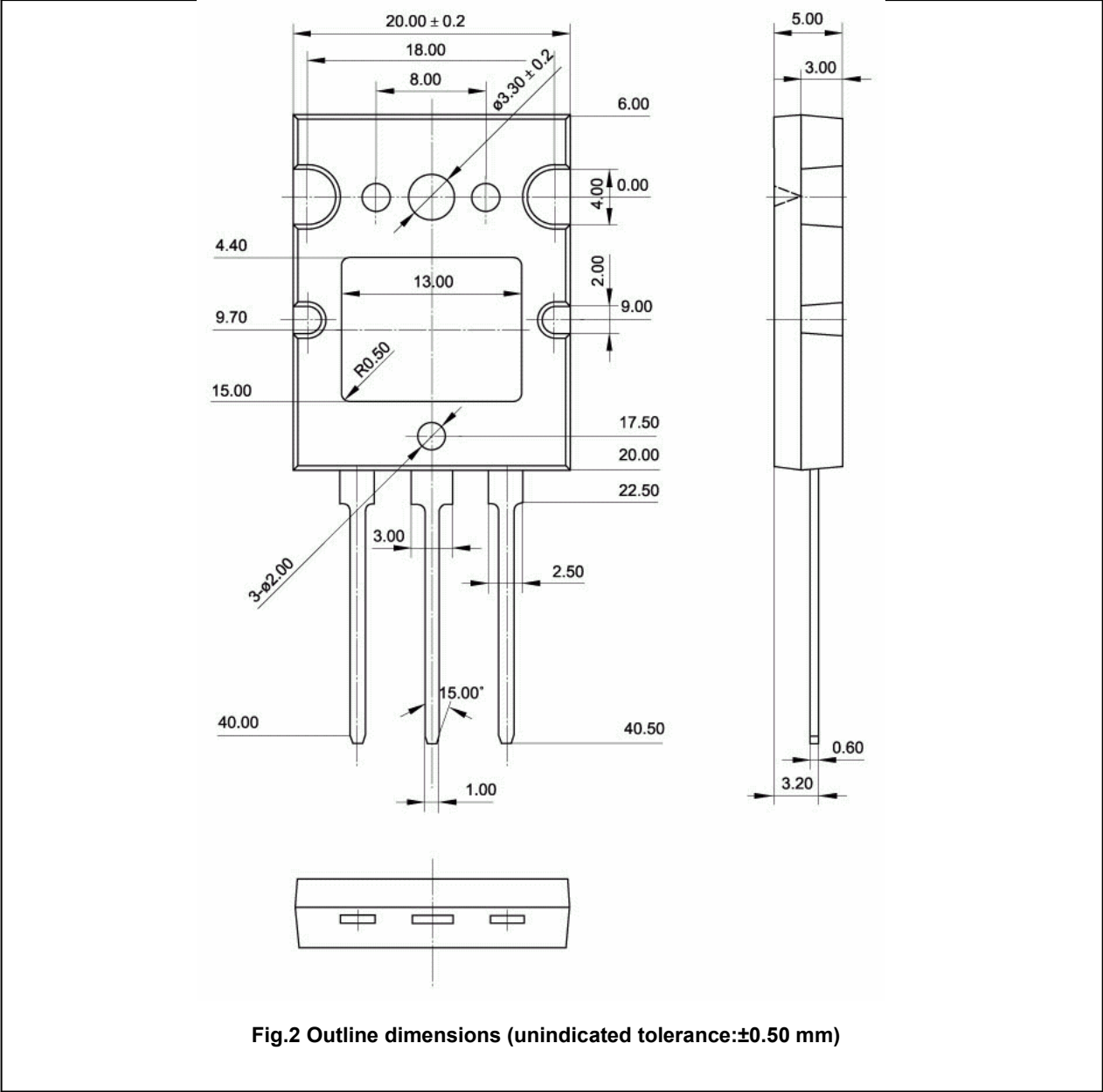


Fig.2 Outline dimensions (unindicated tolerance:±0.50 mm)

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