Leiditech

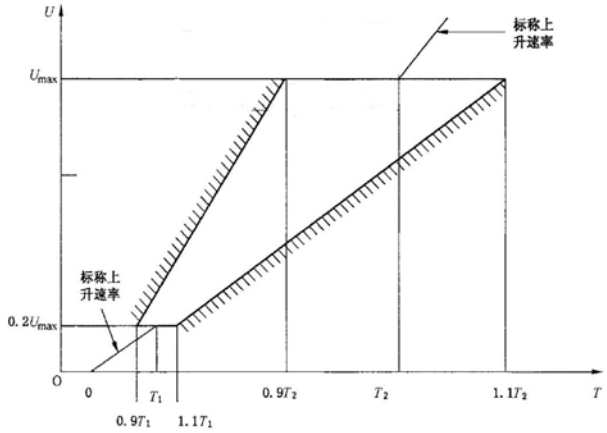
2R600-5S

Features	Applications
● Extremely small size ● Extremely fast response time ● Excellent SMD handing ● Stable performance over life ● Very low capacitance ● High insulation resistance ● Storage and operating temperature -40…+125°C ● RoHS-compatible ● UL-identification	● Splitter ● PCI Cards ● Moden ● Line cards

Electrical specifications

Nominal DC spark-over voltage	600	V
Tolerance	±20	%
Min.	480	V
Max.	720	V
Impulse spark-over voltage		
-at 100 V/µs- for 99% of measured values	< 1000	V
- typical values of distribution	< 800	V
-at 1 kV/µs- for 99% of measured values	< 1100	V
- typical values of distribution	< 900	V
Service life		
10 operations50 Hz, 1 s	5	A
1 operation50 Hz, 0.18 s (9 cycles)	65	A
60 operations [30× (+) & 30× (-)] 8/20 µs	5	kA
1 operation8/20 µs	10	kA
1 operation10/350 µs	1	kA
300 operations10/1000 µs	100	A
Insulation resistance at 100 V_{DC}	> 1	GΩ
Capacitance at 1 MHz	< 1.5	pF
Arc voltage at 1 A	~ 25	V
Glow to arc transition current	< 0.3	A
Glow voltage	~ 160	V

DC breakdown voltage



8/20us, Test wave

T1=1.25T=8us±20%

T2=20us±20%

10/700us,Test Wave

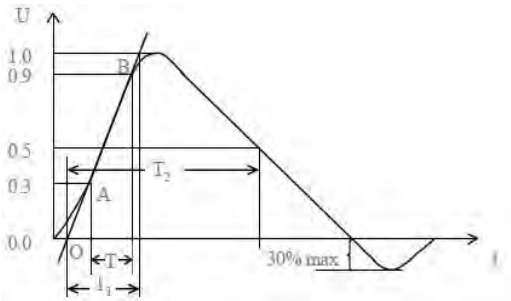
T1=1.67T=10us±20%

T2=700us±20%

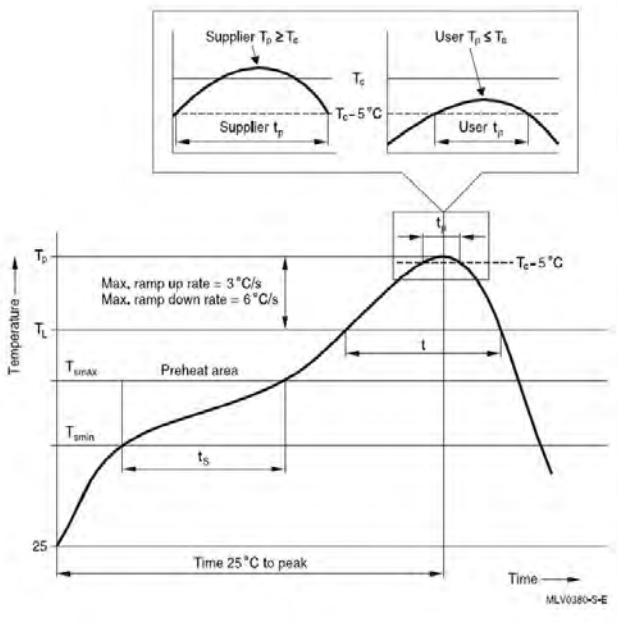
10/1000us,Test Wave

T1=1.67T=10us±20%

T2=1000us±20%



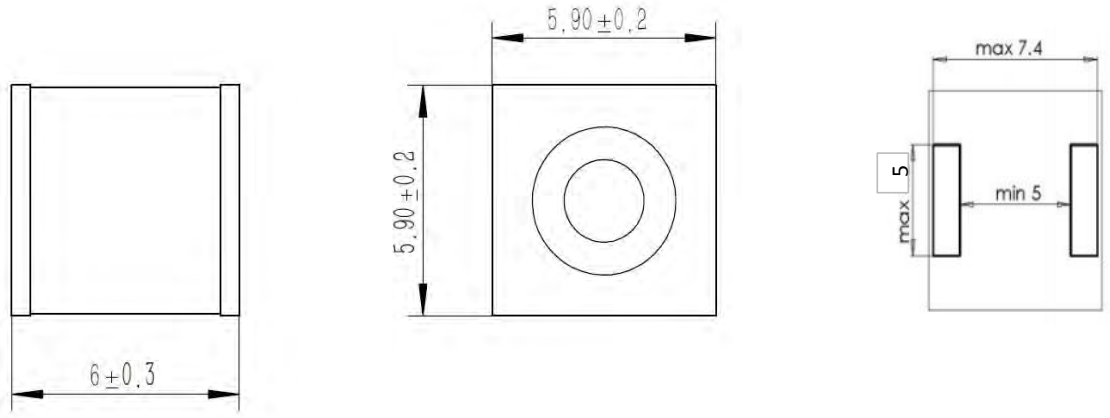
Recommended wave soldering profile



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	 T_{smin} T_{smax} t_{smin} to t_{smax}	 100 °C 150 °C 60 ... 120 s	 150 °C 200 °C 60 ... 180 s
Average ramp-up rate	T_{smax} to T_p	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T_L t_L	183 °C 60 ... 150 s	217 °C 60 ... 150 s
Peak package body temperature *, Classification temperature **	T_p, T_C	220 ... 235 °C **	245 ... 260 °C **
Time (t_p) ** within 5 °C of the specified classification temperature (T_C)		20 s ***	30 s ***
Average ramp-down rate	T_p to T_{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

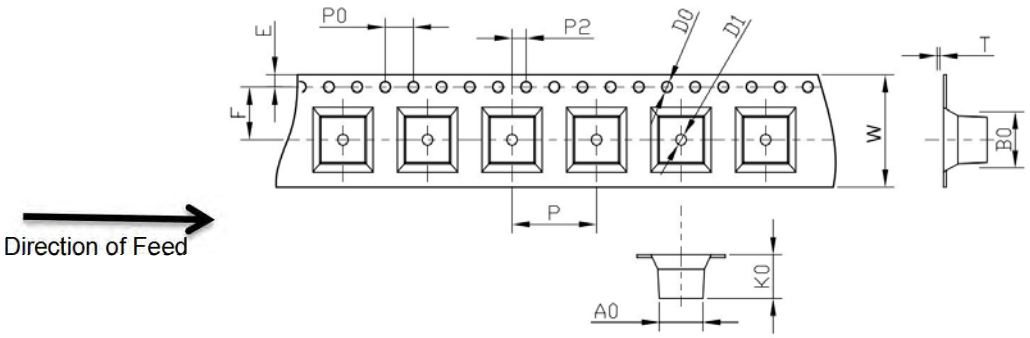
* = Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.
** = For details please refer to JEDEC J-STD-020D.
*** = Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

1) Sampling size in accordance to AQL(C=0)
2) DC spark-over voltage ±30% after load
3) Tests according to ITU-T Rec. K. 12 and IEC61643-1



Packaging

800pcs/reel



16.00 +0.30 -0.10	6.30 +0.10 -0.10	6.40 +0.10 -0.10	6.20 +0.10 -0.10	0.00 +0.00 -0.00	1.75 +0.10 -0.10	7.50 +0.10 -0.10	12.00 +0.10 -0.10	4.00 +0.10 -0.10	2.00 +0.10 -0.10	1.50 +0.10 -0.00	1.50 +0.10 -0.00	0.40 +0.05 -0.05
W	A_0	B_0	K_0	K_1	E	F	P	P_0	P_2	D_0	D_1	T

Cautions and warnings

- Surge arresters must not be operated directly in power supply networks
- Surge arresters may become hot in case of longer periods of current stress (danger of burning).
- If the contacts of the surge arrester are defective, current stress can lead to the formation of sparks and loud noises.
- Surge arresters may be used only within their specified values. In case of overload, the head contacts may fail or the component may be destroyed.
- Damaged surge arresters must not be re-used.

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