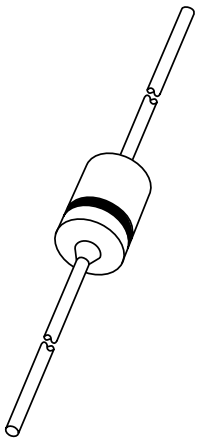
DATA SHEET



1N4150; 1N4151
High-speed diodes

Product specification
Supersedes data of 1996 Sep 03

1999 Jun 01

Philips
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High-speed diodes 1N4150; 1N4151

FEATURES

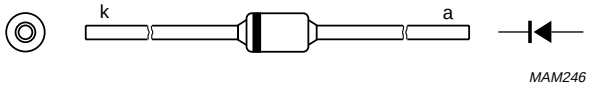
- Hermetically sealed leaded glass SOD27 (DO-35) package
- High switching speed: max. 4 ns
- General application
- Continuous reverse voltage: max. 50 V
- Repetitive peak reverse voltage: max. 75 V
- Repetitive peak forward current: max. 600 mA and 450 mA respectively.

APPLICATIONS

- High-speed switching
- 1N4150: general purpose use in computer and industrial applications
- 1N4151: military and industrial applications.

DESCRIPTION

The 1N4150 and 1N4151 are high-speed switching diodes fabricated in planar technology, and encapsulated in hermetically sealed leaded glass SOD27 (DO-35) packages.



The marking band indicates the cathode.

Fig.1 Simplified outline (SOD27; DO-35) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{RRM}	repetitive peak reverse voltage		–	75	V
V_R	continuous reverse voltage		–	50	V
I_F	continuous forward current	see Fig.2; note 1			
	1N4150		–	300	mA
	1N4151		–	200	mA
I_{FRM}	repetitive peak forward current				
	1N4150		–	600	mA
	1N4151		–	450	mA
I_{FSM}	non-repetitive peak forward current	square wave; T_j = 25 °C prior to surge; see Fig.4			
		t = 1 μs	–	4	A
		t = 1 ms	–	1	A
		t = 1 s	–	0.5	A
P_{tot}	total power dissipation	T_{amb} = 25 °C; note 1	–	500	mW
T_{stg}	storage temperature		–65	+200	°C
T_j	junction temperature		–	200	°C

Note

1. Device mounted on an FR4 printed-circuit board; lead length 10 mm.

ELECTRICAL CHARACTERISTICS

T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_F	forward voltage	see Fig.3			
	1N4150	I_F = 1 mA	540	620	mV
		I_F = 10 mA	660	740	mV
		I_F = 50 mA	760	860	mV
		I_F = 100 mA	820	920	mV
		I_F = 200 mA	870	1000	mV
	1N4151	I_F = 50 mA	–	1000	mV
I_R	reverse current	V_R = 50 V; see Fig.5			
	1N4150		–	0.1	µA
	1N4151		–	0.05	µA
I_R	reverse current	V_R = 50 V; T_j = 150 °C; see Fig.5			
	1N4150		–	100	µA
	1N4151		–	50	µA
C_d	diode capacitance	f = 1 MHz; V_R = 0; see Fig.6			
	1N4150		–	2.5	pF
	1N4151		–	2	pF
t_{rr}	reverse recovery time	when switched from I_F = 10 mA to I_R = 1 mA; R_L = 100 Ω; measured at I_R = 0.1 mA; see Fig.7			
	1N4150		–	6	ns
		when switched from I_F = 10 mA to 200 mA to I_R = 10 mA to 200 mA; R_L = 100 Ω; measured at $I_R = 0.1 \times I_F$; see Fig.7	–	4	ns
		when switched from I_F = 200 mA to 400 mA to I_R = 200 mA to 400 mA; R_L = 100 Ω; measured at $I_R = 0.1 \times I_F$; see Fig.7	–	6	ns
t_{rr}	reverse recovery time	when switched from I_F = 10 mA to I_R = 10 mA; R_L = 100 Ω; measured at I_R = 1 mA; see Fig.7			
	1N4151		–	4	ns
		when switched from I_F = 10 mA to I_R = 60 mA; R_L = 100 Ω; measured at I_R = 1 mA; see Fig.7	–	2	ns
t_{fr}	forward recovery time	when switched to I_F = 200 mA; t_r = 0.4 ns; measured at V_F = 1 V; see Fig.8	–	10	ns

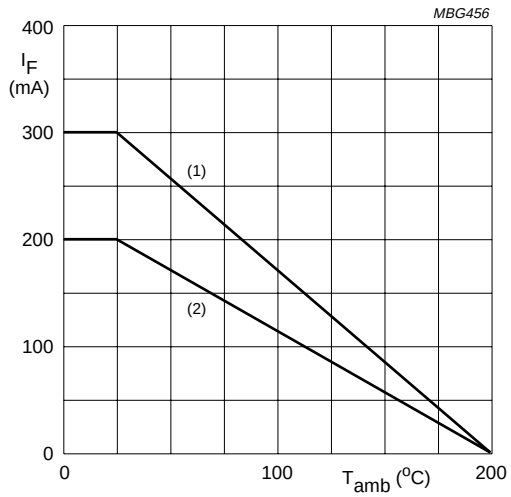
THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}tp}$	thermal resistance from junction to tie-point	lead length 10 mm	240	K/W
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	lead length 10 mm; note 1	350	K/W

Note

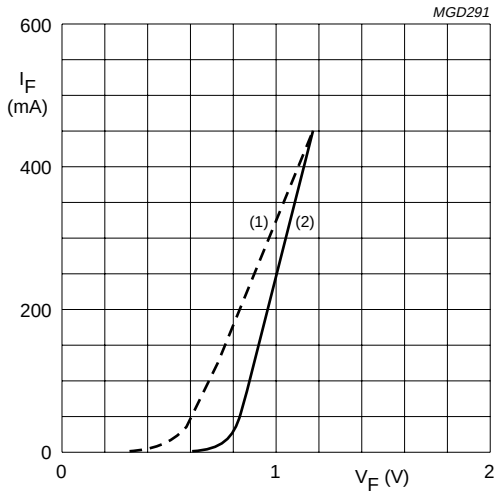
1. Device mounted on a printed circuit-board without metallization pad.

GRAPHICAL DATA



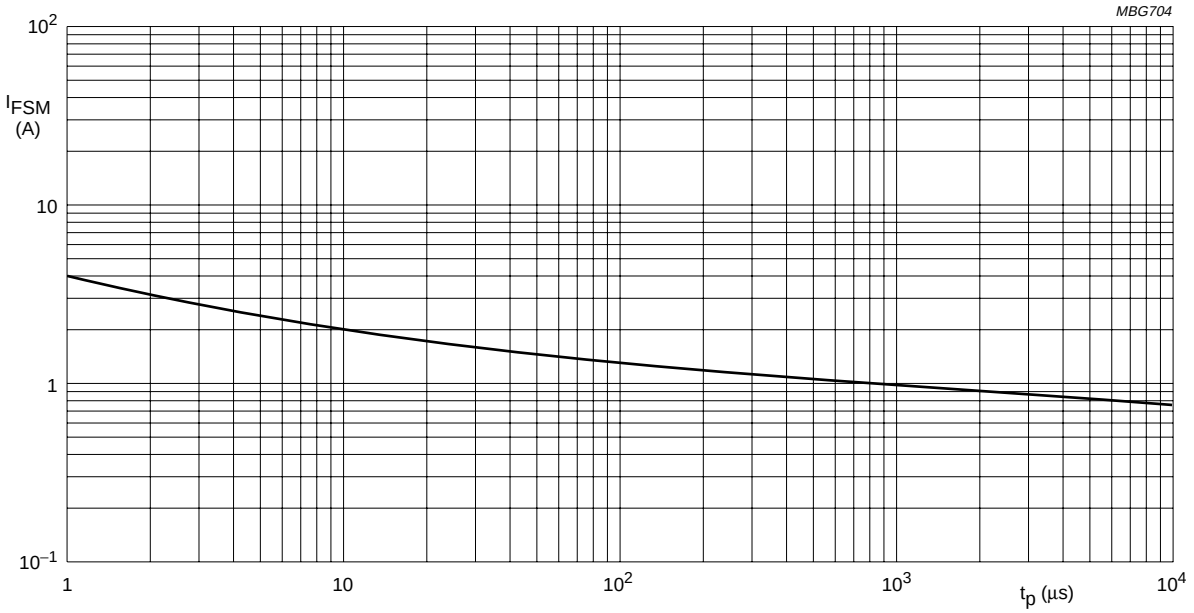
Device mounted on an FR4 printed-circuit board; lead length 10 mm.
(1) 1N4150. (2) 1N4151.

Fig.2 Maximum permissible continuous forward current as a function of ambient temperature.



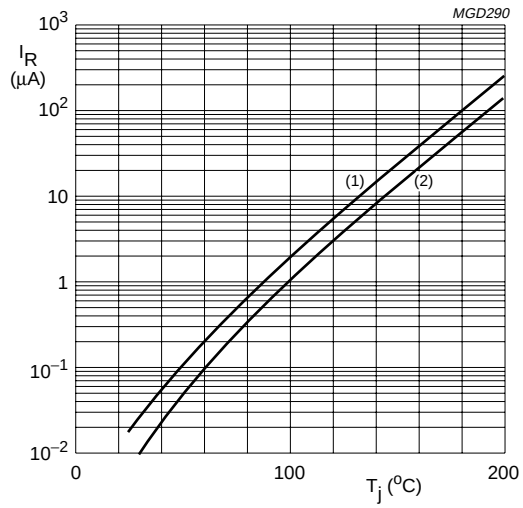
(1) T_j = 175 °C; typical values.
(2) T_j = 25 °C; typical values.

Fig.3 Forward current as a function of forward voltage.



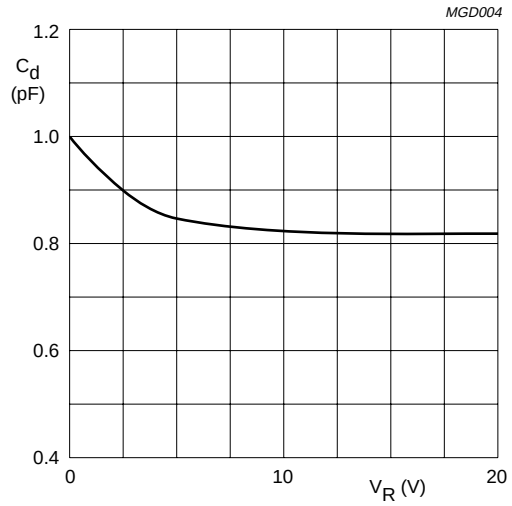
Based on square wave currents.
T_j = 25 °C prior to surge.

Fig.4 Maximum permissible non-repetitive peak forward current as a function of pulse duration.



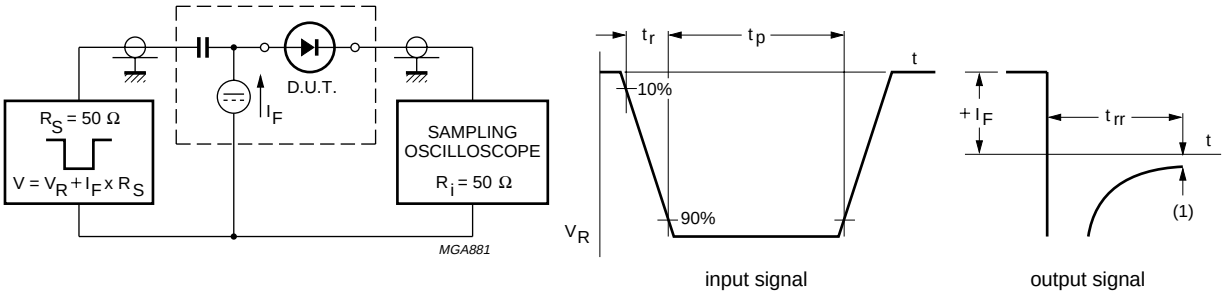
(1) V_R = 75 V; typical values.
(2) V_R = 20 V; typical values.

Fig.5 Reverse current as a function of junction temperature.



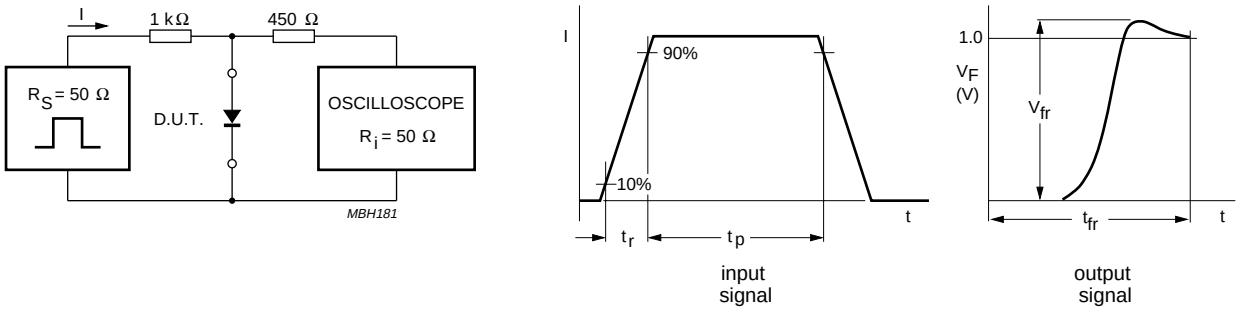
f = 1 MHz; T_j = 25 °C.

Fig.6 Diode capacitance as a function of reverse voltage; typical values.



(1) The value of I_R is dependent on product type.

Fig.7 Reverse recovery voltage test circuit and waveforms.

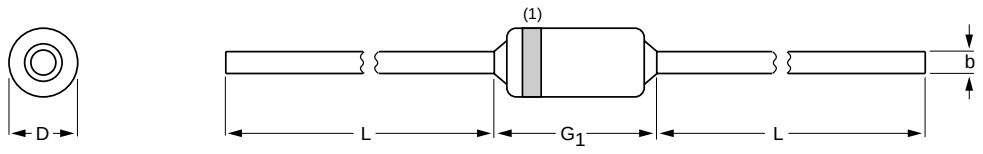


Input signal: forward pulse rise time t_r = 0.4 ns; forward pulse duration t_p = 100 ns; duty factor δ = 0.01.

Fig.8 Forward recovery time test circuit and waveforms.

PACKAGE OUTLINE

Hermetically sealed glass package; axial leaded; 2 leads **SOD27**



DIMENSIONS (mm are the original dimensions)

UNIT	b max.	D max.	G_1 max.	L min.
mm	0.56	1.85	4.25	25.4

0 1 2 mm
scale

Note

1. The marking band indicates the cathode.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOD27	A24	DO-35	SC-40			97-06-09

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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